



SEMICONDUCTOR

2SB1197K

Shandong Yiguang Electronic Joint stock Co., Ltd

TECHNICAL DATA

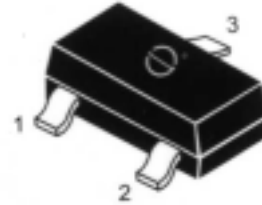
PNP EPITAXIAL SILICON TRANSISTOR

LOW FREQUENCY TRANSISTOR

* Feature:

- (1) Low $V_{ce(sat)}$
 $V_{ce} \leq -0.5V$
 $(I_c/I_b = -0.5A/-50mA)$
- (2) $I_c = -0.8A$
- (3) Complements the 2SD1781K

Package:SOT-23

**ABSOLUTE MAXIMUM RATINGS at Ta=25**

Characteristic	Symbol	Rating	Unit
Collector-Emitter Voltage	V_{ceo}	-32	V
Collector-Base Voltage	V_{cbo}	-40	V
Collector Current	I_c	-0.8	A
Collector Dissipation Ta=25 *	P_D	200	mW
Junction Temperature	T_j	150	
Storage Temperature	T_{stg}	-55-150	

PIN:	1	2	3
STYLE			
NO.1	B	E	C

ELECTRICAL CHARACTERISTICS at Ta=25

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Collector-Base Breakdown Voltage	BV_{cbo}	-40			V	$I_c = -50\mu A$
Collector-Emitter Breakdown Voltage#	BV_{ceo}	-32			V	$I_c = -1mA$
Emitter-Base Breakdown Voltage	BV_{ebo}	-5			V	$I_e = -50\mu A$
Collector-Base Cutoff Current	I_{cbo}			-0.5	μA	$V_{cb} = -20V$
Emitter-Base Cutoff Current	I_{ebo}			-0.5	μA	$V_{eb} = -4V$
DC Current Gain	H_{fe}	120		390		$V_{ce} = -3V$ $I_c = -100mA$
Collector-Emitter Saturation Voltage	$V_{ce(sat)}$			-0.5	V	$I_c = -500mA$ $I_b = -50mA$
Output Capacitance	C_{ob}		12	30	PF	$V_{cb} = -10V$ $I_e = 0$ $f = 1MHz$
Current Gain-Bandwidth Product	f_T	50	200		MHz	$V_{ce} = -5V$ $I_e = 50mA$ $f = 100MHz$

* Total Device Dissipation : FR=1x0.75x0.062in Board,Derate 25 .

Pulse Test: Pulse Width 300 μ S,Duty cycle 2%

DEVICE MARKING:

2SB1197K=AHR



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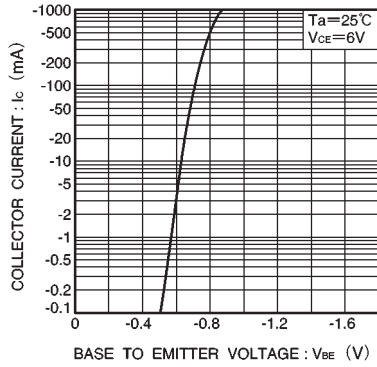


Fig.1 Grounded emitter propagation characteristics

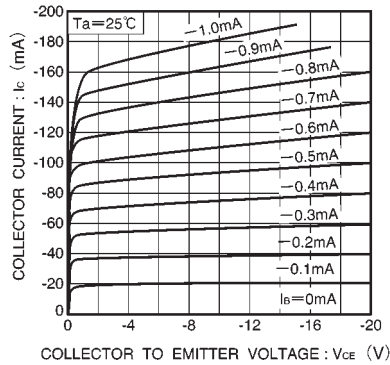


Fig.2 Grounded emitter output characteristics (I)

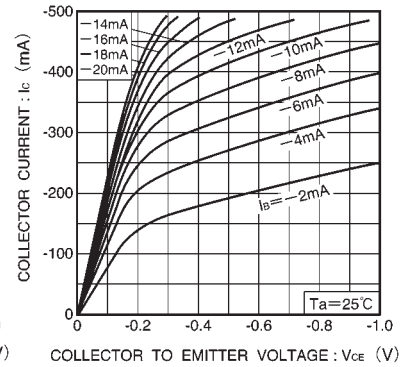


Fig.3 Grounded emitter output characteristics (II)

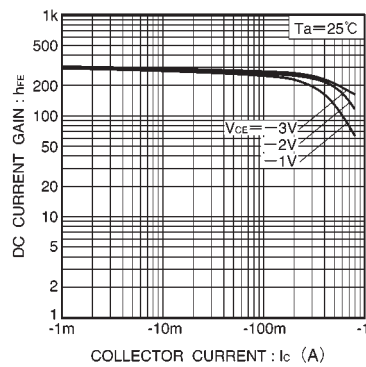


Fig.4 DC current gain vs. collector current

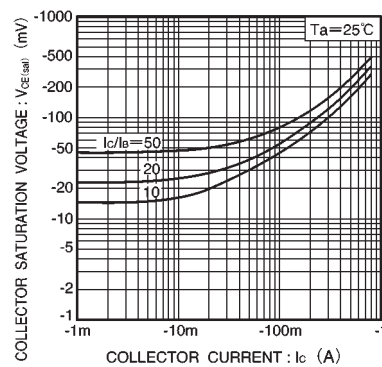


Fig.5 Collector-emitter saturation voltage vs. collector current

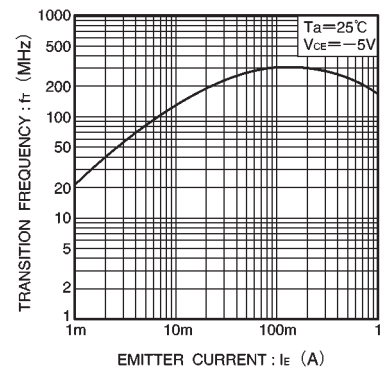


Fig.6 Gain bandwidth product vs. emitter current

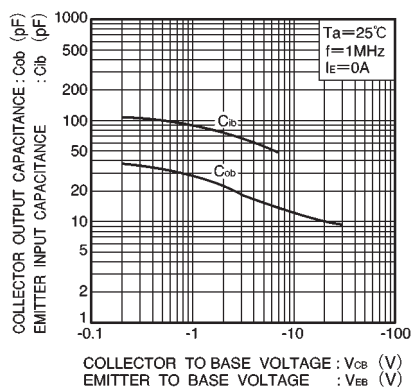


Fig.7 Collector output capacitance vs. collector-base voltage
Emitter input capacitance vs. emitter-base voltage

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