

# High-gain Amplifier Transistor (–32V, –0.3A)

## 2SB852K

### ●Features

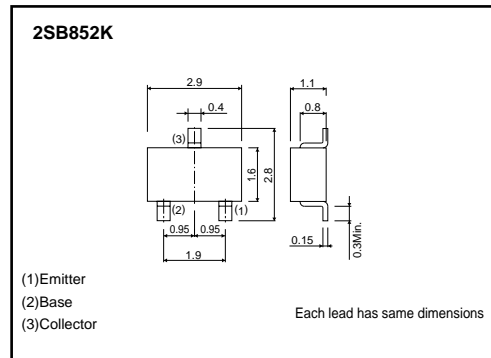
- 1) Darlington connection for high DC current gain.
- 2) Built-in 4kΩ resistor between base and emitter.
- 3) Complements the 2SD1383K.

### ●Packaging specifications

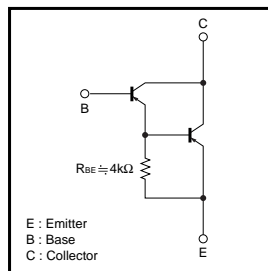
Type	2SB852K
Package	SMT3
hFE	B
Marking	U*
Code	T146
Basic ordering unit (pieces)	3000

\* Denotes hFE

### ●External dimensions (Unit : mm)



### ●Circuit diagram



### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V <sub>CB0</sub>	–40	V
Collector-emitter voltage	V <sub>CES</sub>	–32	V *
Emitter-base voltage	V <sub>EBO</sub>	–6	V
Collector current	I <sub>C</sub>	–0.3	A
Collector power dissipation	P <sub>C</sub>	0.2	W
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature	T <sub>stg</sub>	–55 to +150	°C

\* R<sub>BE</sub>=0Ω

### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV <sub>CB0</sub>	–40	–	–	V	I <sub>C</sub> = –100μA
Collector-emitter breakdown voltage	BV <sub>CES</sub>	–32	–	–	V	I <sub>C</sub> = –1mA
Emitter-base breakdown voltage	BV <sub>EBO</sub>	–6	–	–	V	I <sub>E</sub> = –100μA
Collector cutoff current	I <sub>CB0</sub>	–	–	–1	μA	V <sub>CB</sub> = –24V
Emitter cutoff current	I <sub>EBO</sub>	–	–	–1	μA	V <sub>EB</sub> = –4.5V
DC current transfer ratio	hFE	5000	–	–	–	V <sub>CE</sub> = –5V, I <sub>C</sub> = –0.1A
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	–	–	–1.5	V	I <sub>C</sub> = –200mA, I <sub>B</sub> = –0.4mA *1
Transition frequency	f <sub>T</sub>	–	200	–	MHz	V <sub>CE</sub> = –5V, I <sub>E</sub> =10mA, f=100MHz *2
Output capacitance	C <sub>ob</sub>	–	3	–	pF	V <sub>CB</sub> = –10V, I <sub>E</sub> =0A, f=1MHz

\*1 Measured using pulse current.

\*2 Transition frequency of the device.

Transistors

●Electrical characteristic curves

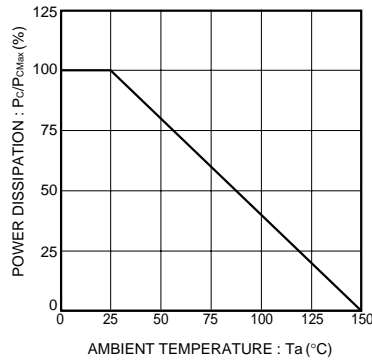


Fig.1 Power dissipation curves

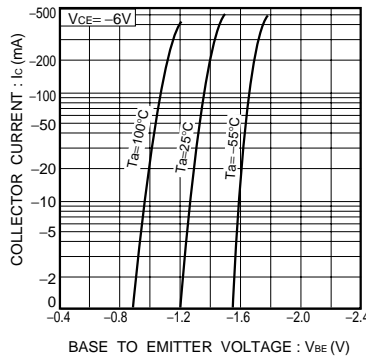


Fig.2 Ground emitter propagation characteristic

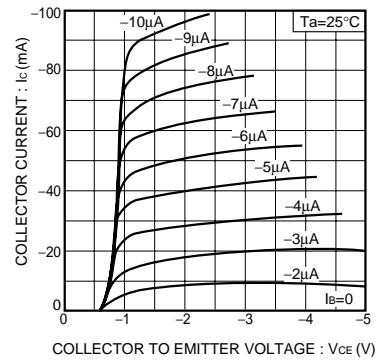


Fig.3 Ground emitter output characteristics

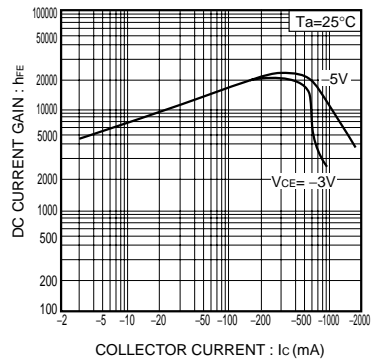


Fig.4 DC current gain vs. collector current ( I )

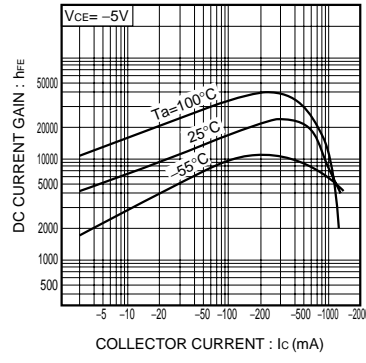


Fig.5 DC current gain vs. collector current ( II )

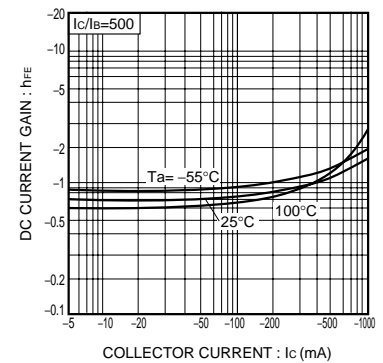


Fig.6 Collector-emitter saturation voltage vs. collector current

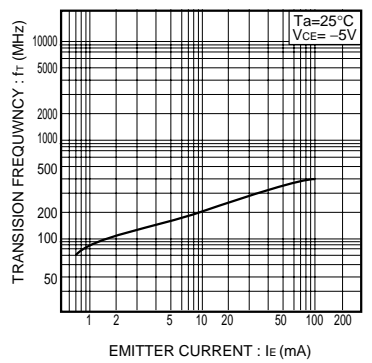


Fig.7 Gain bandwidth product vs. emitter current

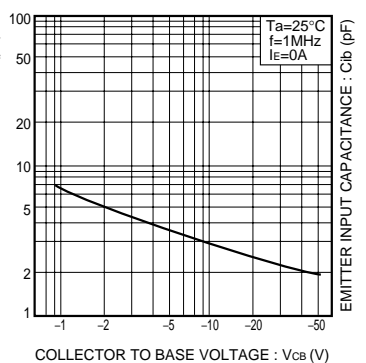


Fig.8 Collector output capacitance vs. collector-base voltage

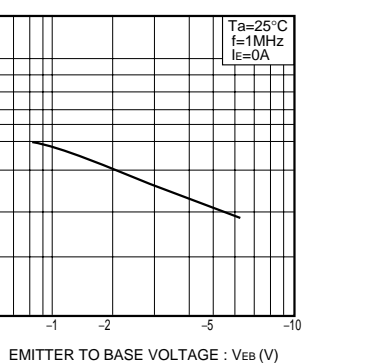


Fig.9 Emitter input capacitance vs. emitter-base voltage

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