



# BC847BPN

45 V, 100 mA NPN/PNP general-purpose transistor

Rev. 04 — 18 February 2009

Product data sheet

## 1. Product profile

### 1.1 General description

NPN/PNP general-purpose transistor pair in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

### 1.2 Features

- Low collector capacitance
- Low collector-emitter saturation voltage
- Closely matched current gain
- Reduces number of components and board space
- No mutual interference between the transistors

### 1.3 Applications

- General-purpose switching and amplification

### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor; for the PNP transistor with negative polarity						
$V_{CE0}$	collector-emitter voltage	open base	-	-	45	V
$I_C$	collector current		-	-	100	mA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}$	200	-	450	

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	emitter TR1		
2	base TR1		
3	collector TR2		
4	emitter TR2		
5	base TR2		
6	collector TR1		

*sym019*

### 3. Ordering information

Table 3. Ordering information

Type number	Package		Version
	Name	Description	
BC847BPN	SC-88	plastic surface-mounted package; 6 leads	SOT363

### 4. Marking

Table 4. Marking codes

Type number	Marking code <sup>[1]</sup>
BC847BPN	13*

- [1] \* = -: made in Hong Kong  
 \* = p: made in Hong Kong  
 \* = t: made in Malaysia  
 \* = W: made in China

### 5. Limiting values

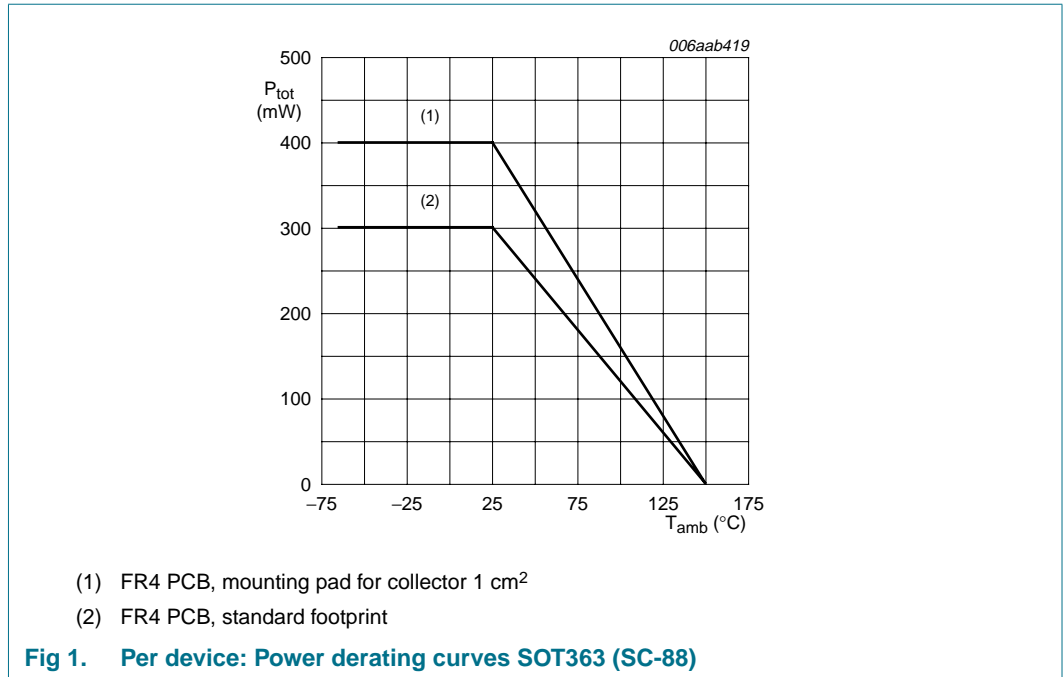
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit	
<b>Per transistor; for the PNP transistor with negative polarity</b>						
$V_{CBO}$	collector-base voltage	open emitter	-	50	V	
$V_{CEO}$	collector-emitter voltage	open base	-	45	V	
$V_{EBO}$	emitter-base voltage	open collector	-	5	V	
$I_C$	collector current		-	100	mA	
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	200	mA	
$I_{BM}$	peak base current	single pulse; $t_p \leq 1$ ms	-	200	mA	
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	220	mW
			[2]	-	250	mW
<b>Per device</b>						
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	300	mW
			[2]	-	400	mW
$T_j$	junction temperature		-	150	°C	
$T_{amb}$	ambient temperature		-65	+150	°C	
$T_{stg}$	storage temperature		-65	+150	°C	

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.



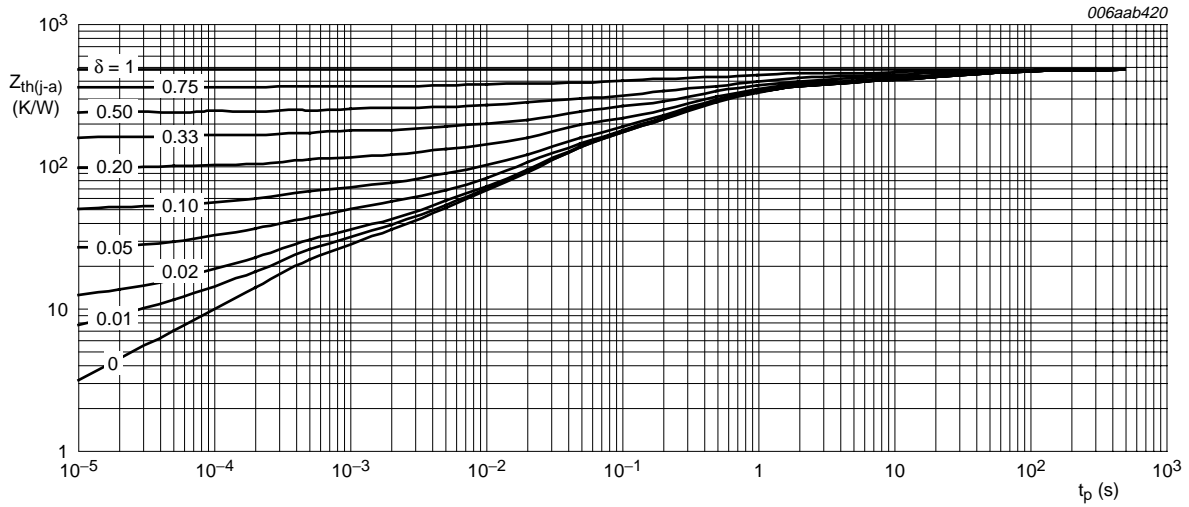
## 6. Thermal characteristics

**Table 6. Thermal characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per transistor</b>						
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	568	K/W
			[2]	-	500	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	-	230	K/W
<b>Per device</b>						
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	416	K/W
			[2]	-	313	K/W

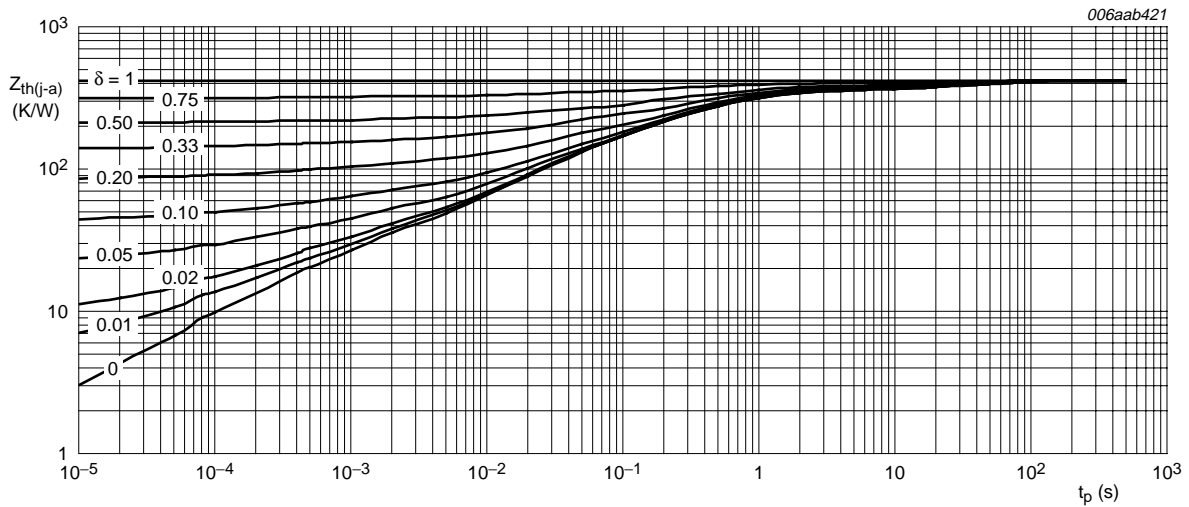
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.



FR4 PCB, standard footprint

**Fig 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**



FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>

**Fig 3. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

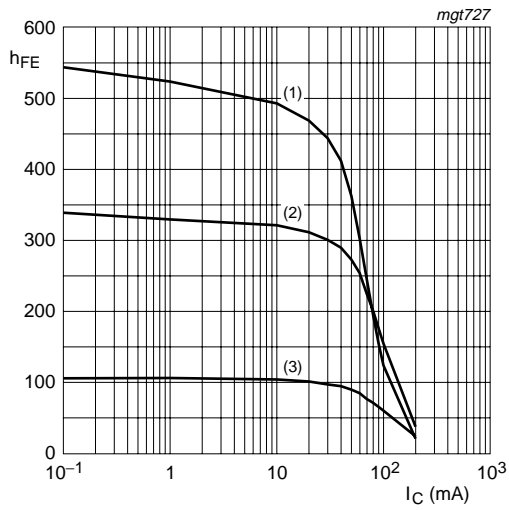
## 7. Characteristics

**Table 7. Characteristics**

$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

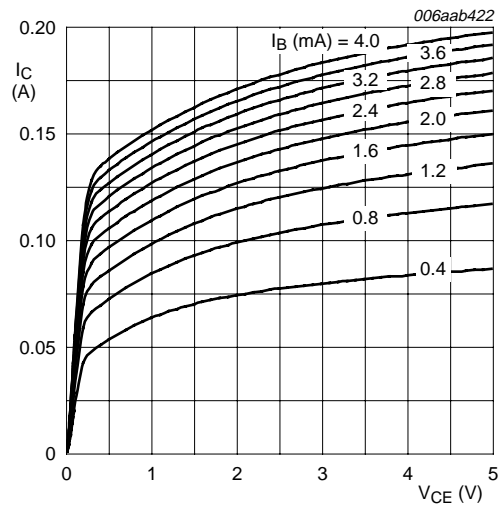
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per transistor; for the PNP transistor with negative polarity</b>						
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 30\text{ V}; I_E = 0\text{ A}$	-	-	15	nA
		$V_{CB} = 30\text{ V}; I_E = 0\text{ A}; T_j = 150\text{ }^{\circ}\text{C}$	-	-	5	$\mu\text{A}$
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 5\text{ V}; I_C = 0\text{ A}$	-	-	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}$	200	-	450	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}$	-	-	100	mV
		$I_C = 100\text{ mA}; I_B = 5\text{ mA}$	[1]	-	300	mV
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}$	-	755	-	mV
$V_{BE}$	base-emitter voltage	$I_C = 2\text{ mA}; V_{CE} = 5\text{ V}$				
	TR1 (NPN)		580	655	700	mV
	TR2 (PNP)		600	655	750	mV
$C_C$	collector capacitance	$I_E = I_e = 0\text{ A}; V_{CB} = 10\text{ V}; f = 1\text{ MHz}$				
	TR1 (NPN)		-	-	1.5	pF
	TR2 (PNP)		-	-	2.2	pF
$C_e$	emitter capacitance	$I_C = I_c = 0\text{ A}; V_{EB} = 0.5\text{ V}; f = 1\text{ MHz}$				
	TR1 (NPN)		-	11	-	pF
	TR2 (PNP)		-	10	-	pF
$f_T$	transition frequency	$I_C = 10\text{ mA}; V_{CE} = 5\text{ V}; f = 100\text{ MHz}$	100	-	-	MHz

[1] Pulse test:  $t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02$ .



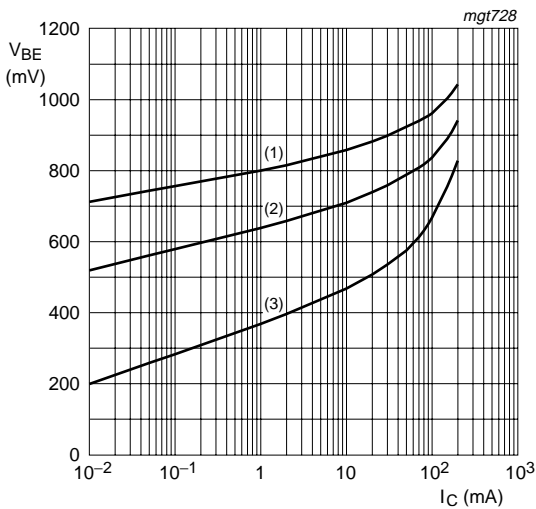
$V_{CE} = 5\text{ V}$   
 (1)  $T_{amb} = 150\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = -55\text{ }^\circ\text{C}$

**Fig 4. TR1 (NPN): DC current gain as a function of collector current; typical values**



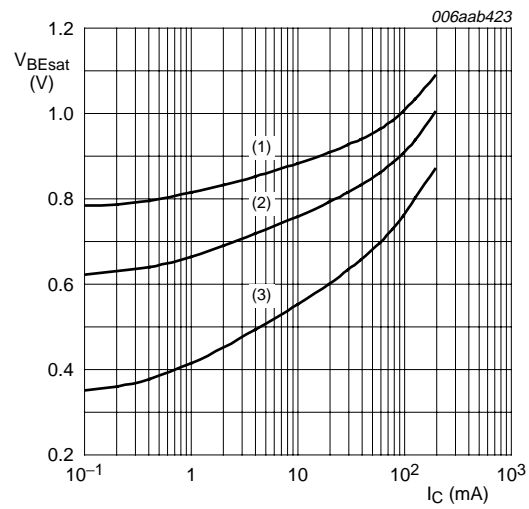
$T_{amb} = 25\text{ }^\circ\text{C}$

**Fig 5. TR1 (NPN): Collector current as a function of collector-emitter voltage; typical values**



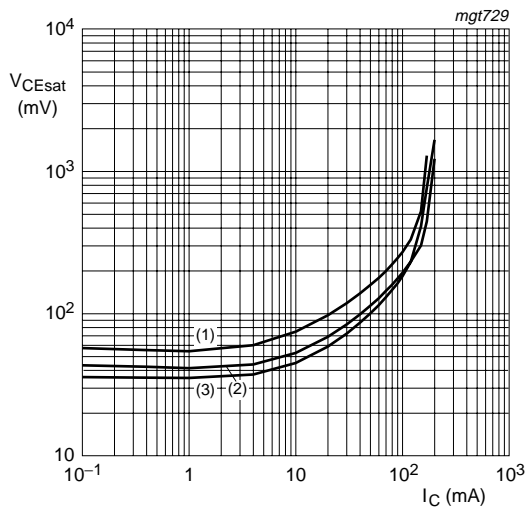
$V_{CE} = 5\text{ V}$   
 (1)  $T_{amb} = -55\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = 150\text{ }^\circ\text{C}$

**Fig 6. TR1 (NPN): Base-emitter voltage as a function of collector current; typical values**



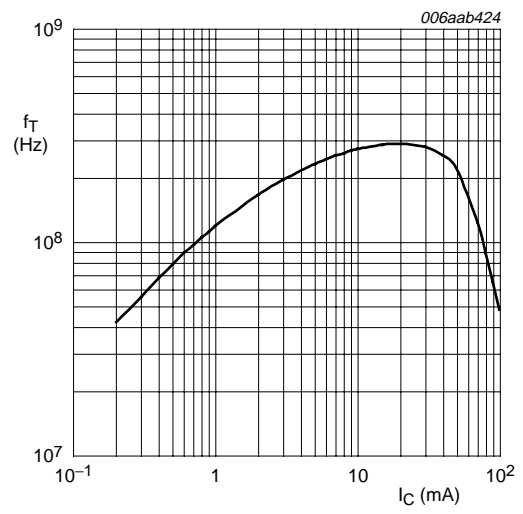
$I_C/I_B = 20$   
 (1)  $T_{amb} = -55\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = 150\text{ }^\circ\text{C}$

**Fig 7. TR1 (NPN): Base-emitter saturation voltage as a function of collector current; typical values**



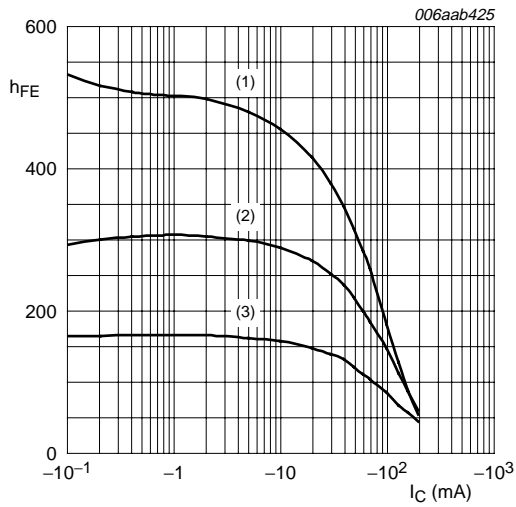
- $I_C/I_B = 20$
- (1)  $T_{amb} = 150^\circ\text{C}$
  - (2)  $T_{amb} = 25^\circ\text{C}$
  - (3)  $T_{amb} = -55^\circ\text{C}$

**Fig 8. TR1 (NPN): Collector-emitter saturation voltage as a function of collector current; typical values**



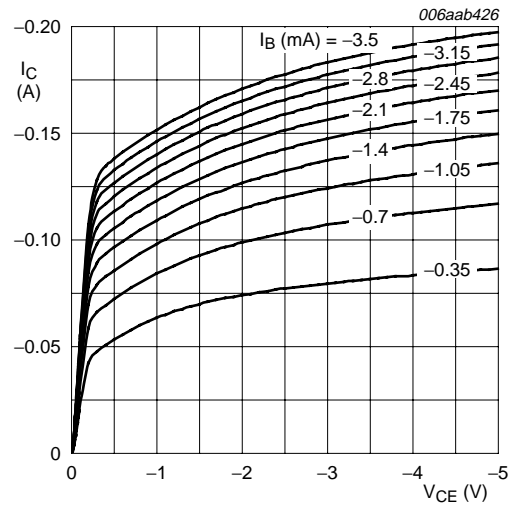
$V_{CE} = 5\text{ V}; f = 1\text{ MHz}; T_{amb} = 25^\circ\text{C}$

**Fig 9. TR1 (NPN): Transition frequency as a function of collector current; typical values**



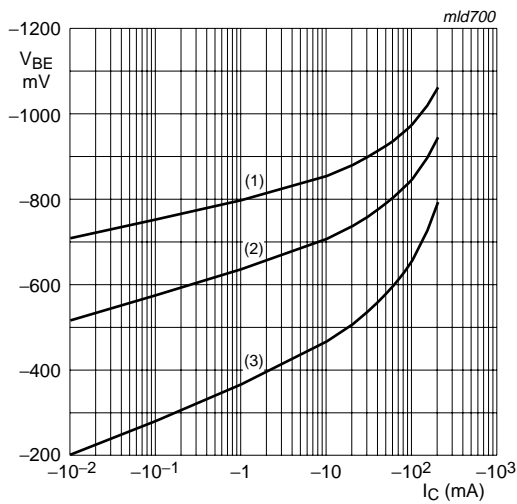
$V_{CE} = -5\text{ V}$   
 (1)  $T_{amb} = 150\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = -55\text{ }^\circ\text{C}$

**Fig 10. TR2 (PNP): DC current gain as a function of collector current; typical values**



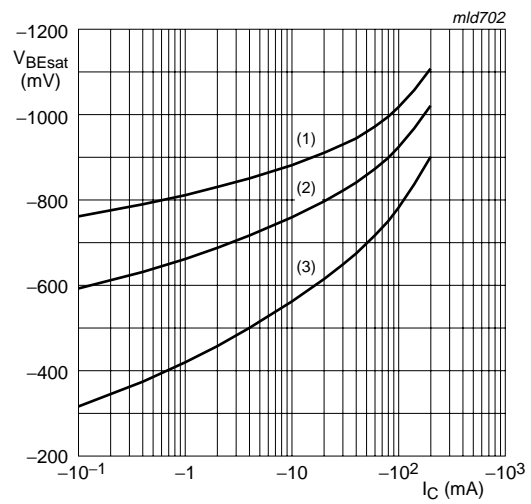
$T_{amb} = 25\text{ }^\circ\text{C}$

**Fig 11. TR2 (PNP): Collector current as a function of collector-emitter voltage; typical values**



$V_{CE} = -5\text{ V}$   
 (1)  $T_{amb} = -55\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = 150\text{ }^\circ\text{C}$

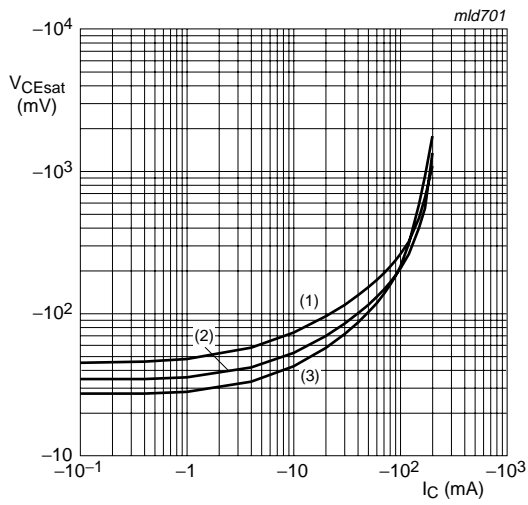
**Fig 12. TR2 (PNP): Base-emitter voltage as a function of collector current; typical values**



$I_C/I_B = 20$   
 (1)  $T_{amb} = -55\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = 150\text{ }^\circ\text{C}$

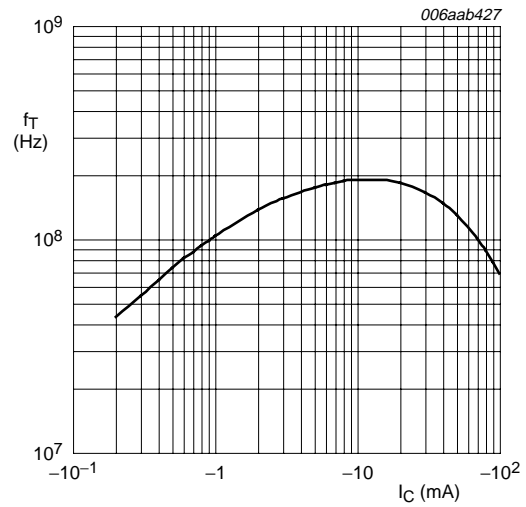
**Fig 13. TR2 (PNP): Base-emitter saturation voltage as a function of collector current; typical values**





- $I_C/I_B = 20$
- (1)  $T_{amb} = 150\text{ °C}$
  - (2)  $T_{amb} = 25\text{ °C}$
  - (3)  $T_{amb} = -55\text{ °C}$

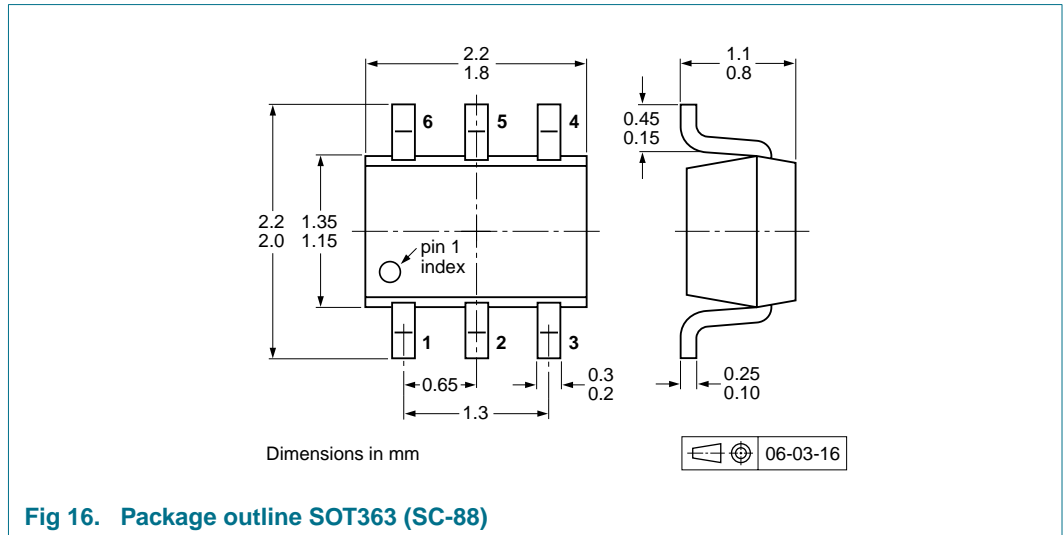
**Fig 14. TR2 (PNP): Collector-emitter saturation voltage as a function of collector current; typical values**



$V_{CE} = -5\text{ V}; f = 1\text{ MHz}; T_{amb} = 25\text{ °C}$

**Fig 15. TR2 (PNP): Transition frequency as a function of collector current; typical values**

## 8. Package outline



## 9. Packing information

**Table 8. Packing methods**

The indicated -xxx are the last three digits of the 12NC ordering code.<sup>[1]</sup>

Type number	Package	Description	Packing quantity	
			3000	10000
BC847BPN	SOT363	4 mm pitch, 8 mm tape and reel; T1	<sup>[2]</sup> -115	-135
		4 mm pitch, 8 mm tape and reel; T2	<sup>[3]</sup> -125	-165

[1] For further information and the availability of packing methods, see [Section 13](#).

[2] T1: normal taping

[3] T2: reverse taping

10. Soldering

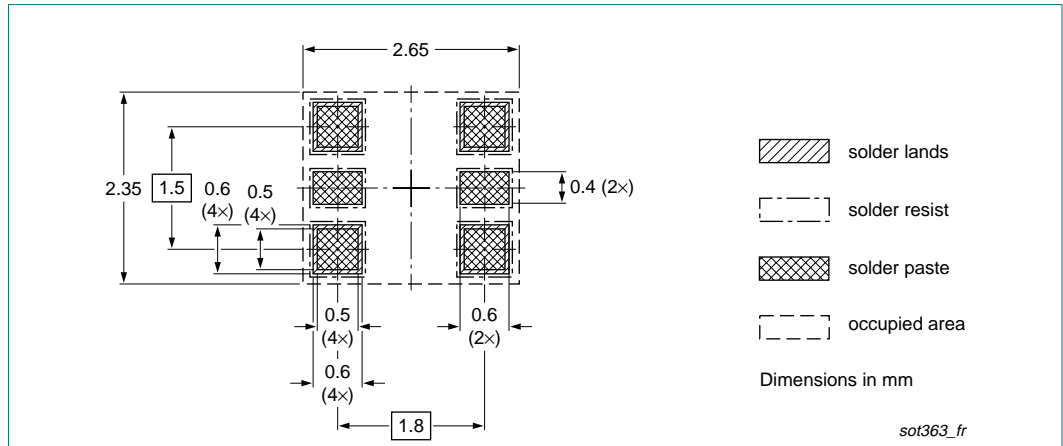


Fig 17. Reflow soldering footprint SOT363 (SC-88)

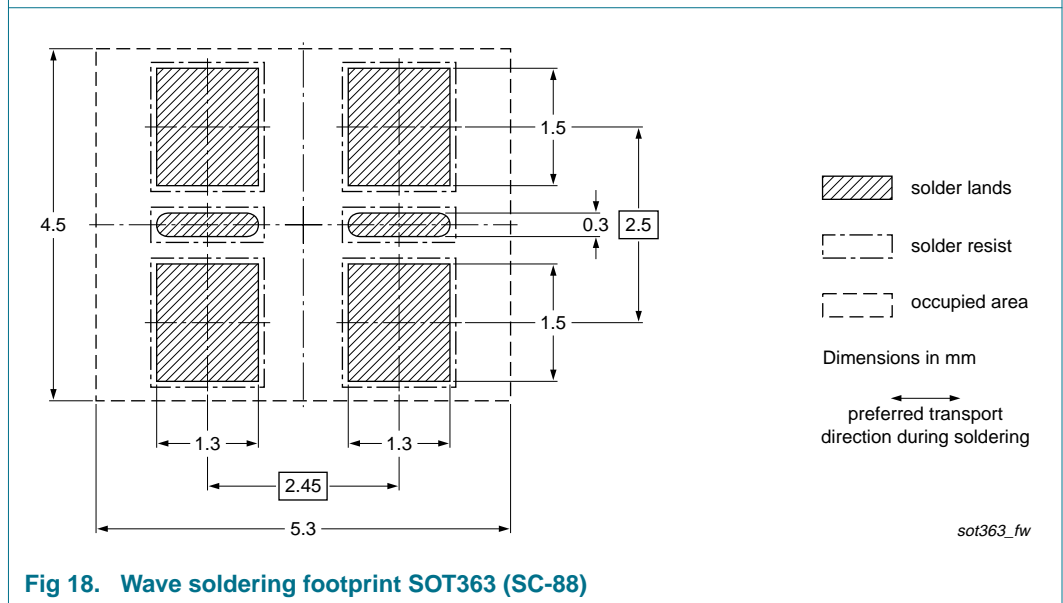


Fig 18. Wave soldering footprint SOT363 (SC-88)

## 11. Revision history

**Table 9. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC847BPN_4	20090218	Product data sheet	-	BC847BPN_3
Modifications:	<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• <a href="#">Section 4 "Marking"</a>: updated</li><li>• <a href="#">Section 7 "Characteristics"</a>: enhanced</li><li>• <a href="#">Section 9 "Packing information"</a>: added</li><li>• <a href="#">Section 10 "Soldering"</a>: added</li><li>• <a href="#">Section 12 "Legal information"</a>: updated</li></ul>			
BC847BPN_3	20011026	Product specification	-	BC847BPN_2
BC847BPN_2	19990426	Preliminary specification	-	BC847BPN_1
BC847BPN_1	19970709	Preliminary specification	-	-

## 12. Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 12.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

### 12.3 Disclaimers

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental

damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

**Terms and conditions of sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

### 12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 13. Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 14. Contents

<b>1</b>	<b>Product profile</b> .....	<b>1</b>
1.1	General description .....	1
1.2	Features .....	1
1.3	Applications .....	1
1.4	Quick reference data .....	1
<b>2</b>	<b>Pinning information</b> .....	<b>1</b>
<b>3</b>	<b>Ordering information</b> .....	<b>2</b>
<b>4</b>	<b>Marking</b> .....	<b>2</b>
<b>5</b>	<b>Limiting values</b> .....	<b>2</b>
<b>6</b>	<b>Thermal characteristics</b> .....	<b>3</b>
<b>7</b>	<b>Characteristics</b> .....	<b>5</b>
<b>8</b>	<b>Package outline</b> .....	<b>10</b>
<b>9</b>	<b>Packing information</b> .....	<b>10</b>
<b>10</b>	<b>Soldering</b> .....	<b>11</b>
<b>11</b>	<b>Revision history</b> .....	<b>12</b>
<b>12</b>	<b>Legal information</b> .....	<b>13</b>
12.1	Data sheet status .....	13
12.2	Definitions .....	13
12.3	Disclaimers .....	13
12.4	Trademarks .....	13
<b>13</b>	<b>Contact information</b> .....	<b>13</b>
<b>14</b>	<b>Contents</b> .....	<b>14</b>

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.



© NXP B.V. 2009.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 18 February 2009

Document identifier: BC847BPN\_4

[www.s-manuals.com](http://www.s-manuals.com)