

# **Voltage Detector IC Series**

# Bipolar Voltage Detector ICs

#### **BD47xx series**

#### General Description

The BD47xx series is a Voltage Detector IC, developed to prevent system errors at transient state, when the power of CPU or logic circuit switches ON/OFF or in cases of momentary shut down. These ICs consist of three terminals (power supply, GND and reset output) to detect power supply voltages and outputs reset signals of various systems. These ICs are ultra-compact and have low current consumption, making them ideal for portable products.

#### Features

- High accuracy detection
- Low current consumption
- Very small package
- Open collector "L" reset output
- Package SSOP5 is similar to SOT-23-5 (JEDEC)

#### Key Specifications

■ Detection voltage range: 1.9V to 4.6V (Typ.)

0.1V steps

±1%

High accuracy detection voltage:Low current consumption:

1.6µA (Typ.)

■ Operating temperature range: -40°C to +75°C

#### Package

SSOP5

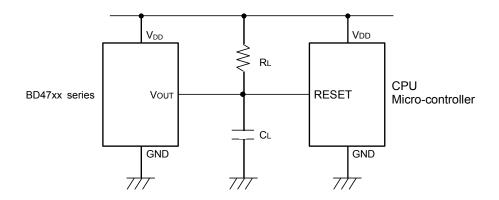


2.90mm x 2.80mm x 1.25mm

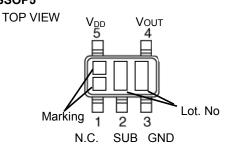
#### Applications

Circuits using microcontrollers or logic circuits that require a reset.

#### Typical Application Circuit



#### Connection Diagram SSOP5

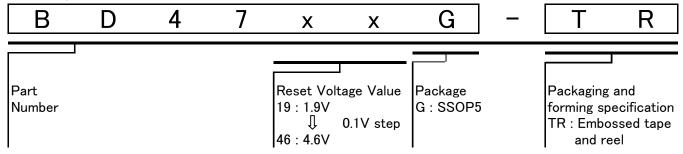


### ●Pin Descriptions

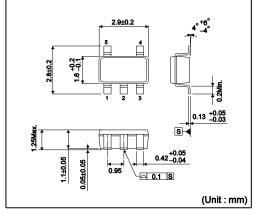
PIN No.	Symbol	Function	
1	N.C.	Unconnected Terminal	
2	SUB	Substrate*	
3	GND	GND	
4	Vout	Reset Output	
5	$V_{DD}$	Power Supply Voltage	

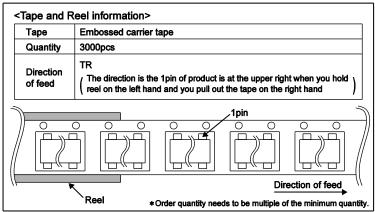
<sup>\*</sup>Substrate Pin should be connected with GND

#### Ordering Information



# **SSOP5** (SOT-23-5)





# ●Lineup

Lineup								
Marking	Detection Voltage	Part Number	Marking	Detection Voltage	Part Number	Marking	Detection Voltage	Part Number
B2	4.6V	BD4746	BR	3.6V	BD4736	BH	2.6V	BD4726
B1	4.5V	BD4745	BQ	3.5V	BD4735	BG	2.5V	BD4725
BZ	4.4V	BD4744	BP	3.4V	BD4734	BF	2.4V	BD4724
BY	4.3V	BD4743	B4	3.3V	BD4733	BE	2.3V	BD4723
BX	4.2V	BD4742	BN	3.2V	BD4732	BD	2.2V	BD4722
BW	4.1V	BD4741	BM	3.1V	BD4731	ВС	2.1V	BD4721
BV	4.0V	BD4740	BL	3.0V	BD4730	BB	2.0V	BD4720
BU	3.9V	BD4739	BK	2.9V	BD4729	BA	1.9V	BD4719
ВТ	3.8V	BD4738	BJ	2.8V	BD4728			
BS	3 7V	BD4737	B3	2 7V	BD4727			

#### Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Power Supply Voltage	$V_{DD}$	-0.3 to +10	٧
Output Voltage	Vouт	-0.3 to +10	٧
Output Current	lo	60	mA
Power Dissipation *1 *2	Pd	540	mW
Operation Temperature Range	Topt	-40 to +75	°C
Ambient Storage Temperature	Tstg	-55 to +125	°C

<sup>\*1</sup> Reduced by 5.4mW/°C when used over 25°C.

● Flectrical characteristics (Unless Otherwise Specified Ta=25°C)

• Electrical characteristics (	Uniess O	inerwise Specified Ta=	25°C)				
Darameter	Course book	Condition		Limit			1.1
Parameter	Symbol			Min.	Тур.	Max.	Unit
	VDET	V <sub>DD</sub> =H→L RL=4.7kΩ		VDET (T)	VDET(T)	VDET(T)	)
				×0.99	VDEI(I)	×1.01	
		VDET=2.5V	Ta=+25°C	2.475	2.5	2.525	V
			Ta=-40°C to 75°C	2.418	-	2.584	
Detection Voltage		VDET=3.0V	Ta=+25°C	2.970	3.0	3.030	
Detection voitage			Ta=-40°C to 75°C	2.901	-	3.100	
		VDET=3.3V	Ta=+25°C	3.267	3.3	3.333	
			Ta=-40°C to 75°C	3.191	-	3.410	
		VDET=4.2V	Ta=+25°C	4.158	4.2	4.242	
			Ta=-40°C to 75°C	4.061	-	4.341	
Temperature Coefficient Of Detection Voltage	VDET/ΔT	R <sub>L</sub> =4.7kΩTa=-20 to+75°C Designed Guarantee		-	±0.01	-	%/°C
Detection Hysteresis Voltage	$\Delta V$ DET	$R_L=4.7k\Omega$ , $V_{DD}=L\rightarrow H\rightarrow$	30	50	100	mV	
Transfer Delay Time "H"	t <sub>PLH</sub>	$C_L = 100 pF, R_L = 4.7 k\Omega$		-	20	50	μs
Transfer Delay Time "L"	t <sub>PHL</sub>	$C_L = 100 pF, R_L = 4.7 k\Omega$		-	60	120	μs
Reset Output Voltage "L"	V <sub>OL</sub>	V <sub>DD</sub> =V <sub>DET</sub> (min.)-0.05V,	-	0.1	0.4	V	
Circuit Current ON	Icc1	V <sub>DD</sub> =V <sub>DET</sub> (min.)-0.05V,	-	1.5	3.0	μΑ	
Circuit Current OFF	lcc2	$V_{DD}=V_{DET}(typ.)/0.85V,$	-	1.6	3.2	μΑ	
Operating Voltage Range	$V_{OPL}$	R <sub>L</sub> =4.7kΩ, V <sub>OL</sub> ≤0.4V		-	0.65	0.85	V
Output Leak Current	I <sub>leak</sub>	V <sub>DD</sub> =V <sub>OUT</sub> =10V	-	-	0.1	μA	
Reset Output Current "L"	I <sub>OL</sub>	Vo=0.4V, V <sub>DD</sub> =V <sub>DET</sub> (mi	3.0	15.0	-	mA	

<sup>\*2</sup> When mounted on ROHM standard circuit board (70mm×70mm×1.6mm, glass epoxy board).

VDET(T):Standard Detection Voltage(1.9V to 4.6V, 0.1V step)
RL:Pull-up resistor to be connected between VOUT and power supply.
CL:Capacitor to be connected between VOUT and GND.
\*1 tplh:VDD=(VDET(typ.)-0.4V)→(VDET(typ.)+0.4V)
\*2 tphl:VDD=(VDET(typ.)+0.4V)→(VDET(typ.)-0.4V)
Design Guarantee. (Outgoing inspection is not done on all products)

# ● Block Diagram

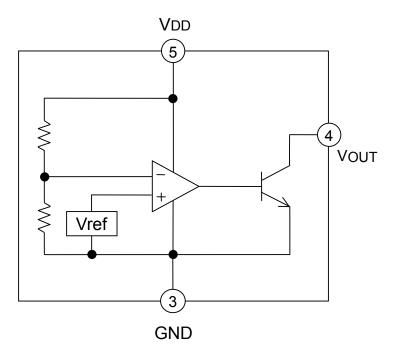


Fig.1 BD47xx series

#### **●**Typical Performance Curves

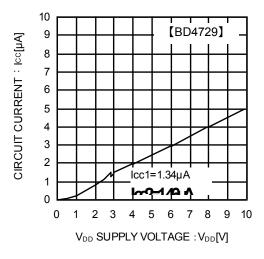


Fig.2 Circuit Current

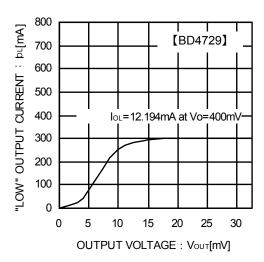


Fig.3 "Low" Output Current

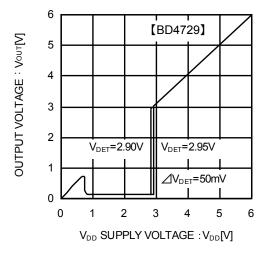


Fig.4 I/O Characteristics

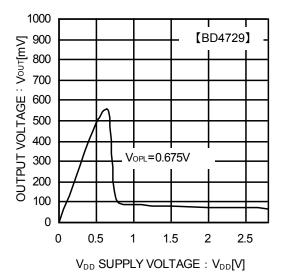


Fig.5 Operating Limit Voltage

#### ● Typical Performance Curves - continued

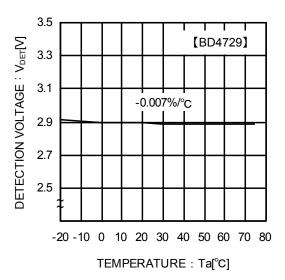


Fig.6 Detection Voltage

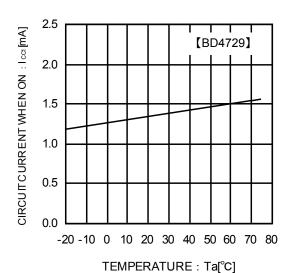


Fig.7 Circuit Current when ON

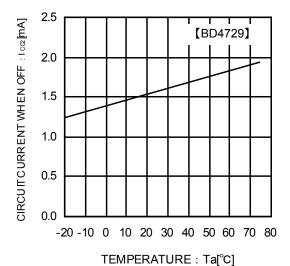


Fig.8 Circuit Current when OFF

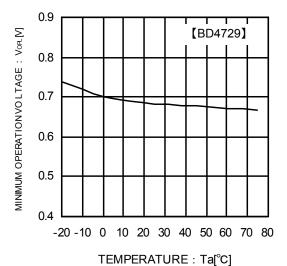


Fig.9 Operating Limit Voltage

#### ● Typical Performance Curves - continued

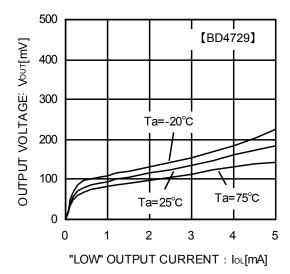


Fig.10 Output Saturation Voltage

#### Application Information

#### **Explanation of Operation**

BD47xx series has threshold voltages namely the detection voltage and release voltage. As the voltages applied to the input reach their respective thresholds, the output switches from "High" to "Low" and from "Low" to "High". The release voltage has a hysteresis that is the value of the detection voltage +50mV (Typ.), preventing chattering in the output. When the input is greater than the release voltage, the output is in a "High" state. When the input decreases from that state, the output switches to "Low" upon reaching the detection voltage. When the input is less than the detection voltage, the output is in a "Low" state. When the input increases from that state, the output switches to "High" upon reaching the release voltage. Additionally, at least 0.85V input voltage is required for the circuit to function as expected. When the input falls below the operating limit voltage, the output becomes unstable.

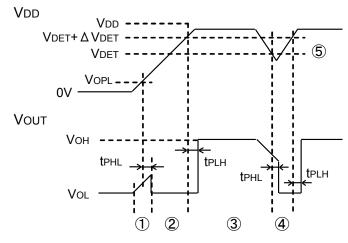


Fig.11 Timing Waveform

- When the power supply is turned on, the output is still unstable until it reaches the operating limit voltage ( $V_{OPL}$ ) with a given time  $t_{PHL}$ . Therefore it is possible that the reset signal is not outputted when the rise time of  $V_{DD}$  is faster than  $t_{PHL}$ .
- When  $V_{DD}$  is greater than  $V_{OPL}$  but less than the reset release voltage (VDET+ $\Delta$ VDET), the output voltages will switch to Low.
- $^{\textcircled{3}}$  If  $V_{DD}$  exceeds the reset release voltage (VDET+ $\Delta$ VDET) then  $V_{OUT}$  switches from L to H after  $t_{PLH}$ .
- If  $V_{DD}$  drops below the detection voltage (VDET) when the power supply is powered down or when there is a power supply fluctuation,  $V_{OUT}$  switches to L (with a delay of  $t_{PHL}$ ).
- $^{(5)}$  The potential difference between the detection voltage and the release voltage is known as the Hysteresis Width ( $\Delta$ VDET). The system is designed such that, the output does not toggle with power supply fluctuations within this hysteresis width, malfunctions due to noise are prevented.

#### <Pre><Pre>cautions>

Please be aware that when there is resistance on the power supply line, the detection voltage varies with voltage drops caused by the IC current consumption.

Please connect a capacitor between V<sub>DD</sub> and GND when the power supply line has high impedance.

#### Circuit Applications

The following is an example of an application circuit using Reset IC for normal power supply detection. BD47xx series requires a pull up resistor on the output terminal. The pull up resistor value should be decided depending on the application, with enough consideration of power supply level and output current capability. When a capacitor is placed at the output terminal, to delay the output time or to remove noise, the output will become slower during starting or stopping. Please be careful in considering the appropriate value for pull up resistors, output current, and capacitor when inserting a bypass capacitor between input and GND. Please be aware that if an extremely large capacitor is used, the response time will become excessively slow.

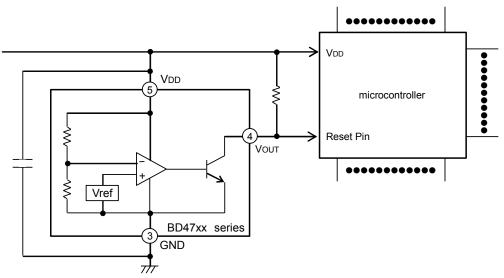


Fig.12 Circuit Applications

#### Operational Notes

#### 1) Absolute maximum ratings

Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

#### 2) Ground Voltage

The voltage of the ground pin must be the lowest voltage of all pins of the IC at all operating conditions. Ensure that no pins are at a voltage below the ground pin at any time, even during transient condition.

#### 3) Recommended operating conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

#### 4) Bypass Capacitor for Noise Rejection

To help reject noise, put a 1μF capacitor between VDD pin and GND and 1000pF capacitor between VOUT pin and GND. Be careful when using extremely big capacitor as transient response will be affected.

#### 5) Short between pins and mounting errors

Be careful when mounting the IC on printed circuit boards. The IC may be damaged if it is mounted in a wrong orientation or if pins are shorted together. Short circuit may be caused by conductive particles caught between the pins.

#### 6) Operation under strong electromagnetic field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

- 7) The V<sub>DD</sub> line impedance might cause oscillation because of the detection current.
- 8) A V<sub>DD</sub> to GND capacitor (as close connection as possible) should be used in high V<sub>DD</sub> line impedance condition.
- 9) Lower than the mininum input voltage puts the VouT in high impedance state, and it must be VDD in pull up (VDD) condition.

#### 10) External parameters

The recommended parameter range for  $R_L$  is  $2k\Omega$  to  $1M\Omega$ . There are many factors (board layout, etc) that can affect characteristics. Please verify and confirm using practical applications.

#### 11) Power on reset operation

Please note that the power on reset output varies with the  $V_{DD}$  rise time. Please verify the behavior in the actual operation.

#### 12) Testing on application boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

#### 13) Rush current

When power is first supplied to the IC, rush current may flow instantaneously. It is possible that the charge current to the parasitic capacitance of internal photo diode or the internal logic may be unstable. Therefore, give special consideration to power coupling capacitance, power wiring, width of GND wiring, and routing of connections.

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CLASSⅢ	CL ACCIII	CLASS II b	CI VCCIII
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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