-100mA / -50V Digital transistors (with built-in resistors) DTA114YUB

Applications

Inverter, Interface, Driver

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP silicon epitaxial planar transistor type (Resistor built-in)

Packaging specifications

	Package	UMT3F
	Packaging type	Taping
	Code	TL
Part No.	Basic ordering unit (pieces)	3000
DTA114YUB		0

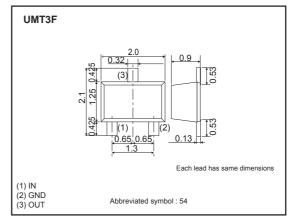
Absolute maximum ratings (Ta=25°C)

30							
Parameter	Symbol	Limits	Unit				
Supply voltage	Vcc	-50	V				
Input voltage	VIN	-40 to +6	V				
Collector current	IC(max.) ^{*1}	-100	mA				
Output current	lo	-70	mA				
Power dissipation	PD *2	200	mW				
Junction temperature	Tj	150	°C				
Range of storage temperature	Tstg	-55 to +150	°C				

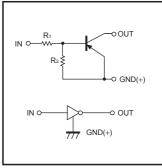
*1 Characteristics of built-in transistor

*2 Each terminal mounted on a recommended land

Dimensions (Unit : mm)



Equivalent circuit



R1=10k Ω , R2=47k Ω

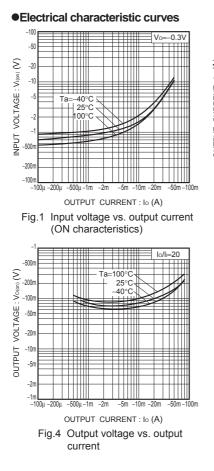
DTA114YUB

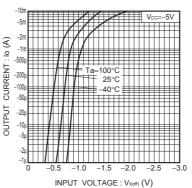
Transistors

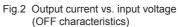
•Electrical characteristics (Ta=25°C)

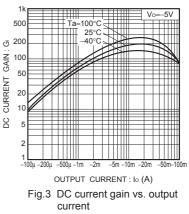
Symbol	Min.	Тур.	Max.	Unit	Conditions
VI(off)	-	-	-0.3	V	Vcc=-5V, Io=-100µA
VI(on)	-1.4	-	-		Vo=-0.3V, Io=-1mA
VO(on)	-	-100	-300	mV	lo=–5mA, l⊫–0.25mA
h	-	-	-880	μA	VI=-5V
IO(off)	-	-	-500	nA	Vcc=-50V, Vi=0V
Gı	68	-	_	-	Vo=-5V, Io=-5mA
f⊤ ∗	-	250	-	MHz	Vce=-10V, Ie=5mA, f=100MHz
R1	7	10	13	kΩ	-
R2/R1	3.7	4.7	5.7	-	_
	VI(off) VI(on) VO(on) II IO(off) GI fT * R1	$\begin{array}{c c} V_{i(off)} & - \\ \hline V_{i(on)} & -1.4 \\ \hline V_{0(on)} & - \\ \hline I_{1} & - \\ \hline I_{0(off)} & - \\ \hline G_{1} & 68 \\ \hline f_{T} * & - \\ \hline R_{1} & 7 \\ \end{array}$	$\begin{array}{c cccc} V_{i(off)} & - & - \\ \hline V_{i(on)} & -1.4 & - \\ \hline V_{0(on)} & - & -100 \\ \hline I_{1} & - & - \\ \hline I_{0(off)} & - & - \\ \hline G_{1} & 68 & - \\ \hline f_{T} * & - & 250 \\ \hline R_{1} & 7 & 10 \\ \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

* Characteristics of built-in transistor









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