# 100mA / 50V Digital transistor (with built-in resistor) DTC125TUA / DTC125TKA / DTC125TSA

# Applications

Inverter, Interface, Driver

## Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- Only the on / off conditions need to be set for operation, making the device design easy.
- 4) Higher mounting densities can be achieved.

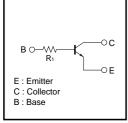
## Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

#### Packaging specifications

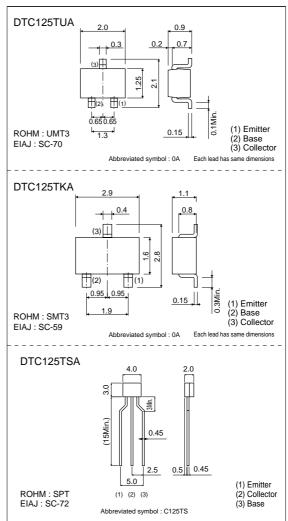
	Package	UMT3	SMT3	SPT				
	Packaging type	Taping	Taping	Taping				
	Code	T106	T146	TP				
Part No.	Basic ordering unit (pieces)	3000	3000	5000				
DTC125TUA		0	-	-				
DTC125TKA		-	0	-				
DTC125TSA		-	-	0				

#### Equivalent circuit



R1=200kΩ

# •External dimensions (Unit : mm)



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# Transistors

# •Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	50	V
Collector-emitter voltage		Vceo	50	V
Emitter-base voltage		Vebo	5	V
Collector current		lc	100	mA
Collector power dissipation	DTC125TUA / DTC125TKA	De	200	
	DTC125TSA	Pc	300	mW
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55 to +150	°C

# •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	50	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	5	-	-	V	Ιε=50μΑ
Collector cutoff current	Ісво	-	-	0.5	μA	Vcb=50V
Emitter cutoff current	Іево	-	-	0.5	μA	VEB=4V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.3	V	Ic=0.5mA , Iв=0.05mA
DC current transfer ratio	hfe	100	250	600	-	Ic=1mA , Vce=5V
Input resistance	R1	140	200	260	kΩ	-
Transition frequency	f⊤ *	-	250	_	MHz	Vce=10V , Ie= -5mA , f=100MHz

\* Characteristics of built-in transistor

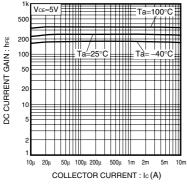


Fig.1 DC current gain

vs. Collector current

# •Electrical characteristic curves

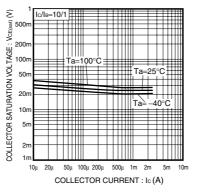


Fig.2 Collector-Emitter saturation voltage vs. Collector current

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