DTC143XUB

# 100mA / 50V Digital transistors (with built-in resistors) DTC143XUB

# Applications

Inverter, Interface, Driver

# Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

#### Structure

NPN silicon epitaxial planar transistor type (Resistor built-in)

## •Packaging specifications

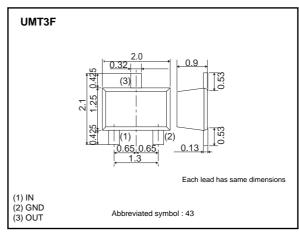
	Package	UMT3F
	Packaging type	Taping
	Code	TL
Part No.	Basic ordering unit (pieces)	3000
DTC143XUB		0

## ● Absolute maximum ratings (Ta=25°C)

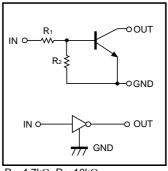
Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	50	V
Input voltage	Vin	-7 to +20	V
Collector current	lc(max)*1	100	mA
Output current	lo	100	mA
Power dissipation	P <sub>D</sub> ∗2	200	mW
Junction temperature	Tj	150	°C
Range of storage temperature	Tstg	-55 to +150	°C

\*1 Characteristics of built-in transistor \*2 Each terminal mounted on a recommended land

#### •Dimensions (Unit : mm)



#### Equivalent circuit



R1=4.7kΩ, R2=10kΩ

# Transistors

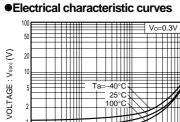
# DTC143XUB

# •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	0.3	v	Vcc=5V, Io=100µA
	VI(on)	2.5	-	-		Vo=0.3V, Io=20mA
Output voltage	VO(on)	-	100	300	mV	lo=10mA, l⊫0.5mA
Input current	h	-	-	1.8	mA	Vi=5V
Output current	IO(off)	-	-	500	nA	Vcc=50V, VI=0V
DC current gain	Gı	30	-	-	-	Vo=5V, lo=10mA
Transition frequency	f⊤ *	-	250	-	MHz	Vce=10V, Ie=-5mA, f=100MHz
Input resistance	R1	3.29	4.7	6.11	kΩ	_
Resistance ratio	R2/R1	1.7	2.1	2.6	-	_

\* Characteristics of built-in transistor

INPUT



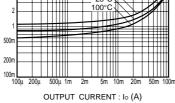


Fig.1 Input voltage vs. output current (ON characteristics)

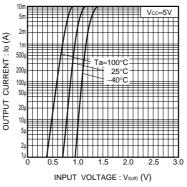
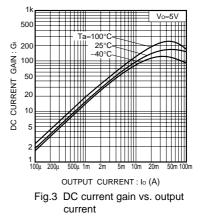
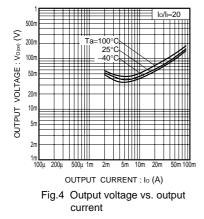


Fig.2 Output current vs. input voltage (OFF characteristics)





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Appendix1-Rev2.0

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