Dual digital transistors

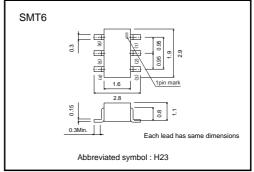
Features

- In addition to the features of regular digital transistors.
- 1) Low saturation voltage, typically
- VCE (sat) =40mV at Ic / IB=50mA / 2.5mA, makes these transistors ideal for muting circuits.
- 2) These transistors can be used at high current levels, Ic=600mA.
- 3) Two DTC643T chips in a SMT package.

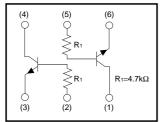
Structure

NPN digital transistor (Built-in resistor type)

•External dimensions (Unit : mm)



Equivalent circuit



Parameter	Symbol	Limits	Unit		
Collector-base voltage	Vсво	20	V		
Collector-emitter voltage	Vceo	20	V		
Emitter-base voltage	VEBO	12	V		
Collector current	lc	600	mA		
Collector power dissipation	Pc	300(TOTAL)	mW *		
Junction temperature	Tj	150	°C		

Tstg

-55 to +150

•Absolute maximum ratings (Ta=25°C)

* 200mW per element must not be a exceeded.

Storage temperature

°C

IMH23

Transistor

•Electrical characteristics (Ta=25°C)

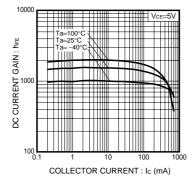
	,					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	20	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	20	-	-	V	Ic=1mA
Emitter-base breakdown voltage	BVEBO	12	-	-	V	I _E =50μA
Collector cutoff current	Ісво	-	-	0.5	μA	V _{CB} =20V
Emitter cutoff current	I _{EBO}	-	-	0.5	μA	V _{EB} =12V
Collector-emitter saturation voltage	VCE (sat)	-	40	150	mV	Ic / I _B =50mA / 2.5mA
DC current transfer ratio	h _{FE}	820	-	2700	_	V _{CE} =5V, I _C =50mA
Input resistance	R1	3.29	4.7	6.11	kΩ	_
Transition frequency	f⊤	-	150	-	MHz	V _{CE} =10V, I _E =-50mA, f=100MHz *
Output "ON" resistance	Ron	-	0.55	-	Ω	VI=5V, R∟=1kΩ, f=1KHz
*Transition fraguency of the device						

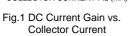
*Transition frequency of the device.

Packaging specifications and hFE

	Package	SMT6
Туре	Packaging type	Taping
	Code	T110
	Basic ordering unit (pieces)	3000
IMH23		0

Electrical characteristic curves





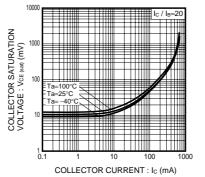
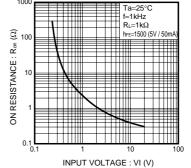


Fig.2 Collector-Emitter Saturation

Voltage vs. Collector Current



1000

Fig.3 "ON" resistance vs. Input Voltage

•Ron measurement circuit

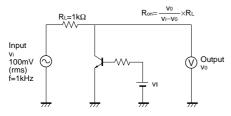


Fig.4 Output "ON" resistance (Ron) measurement circuit

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