# General purpose (dual transistors)

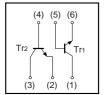
#### Features

- 1) Two 2SC3906K chips in an SMT package.
- 2) High breakdown voltage.

### •Package, marking, and packaging specifications

Part No.	IMX8
Package	SMT6
Marking	X8
Code	T108
Basic ordering unit (pieces)	3000

### •Equivalent circuit



#### Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	120	V	
Collector-emitter voltage	Vceo	120	V	
Emitter-base voltage	Vево	5	V	
Collector current	lc	50	mA	
Power dissipation	Pc	300(TOTAL)	mW *	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

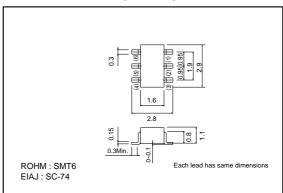
\* 200mW per element must not be exceeded.

# •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	120	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	120	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	5	-	-	V	Iε=50μA
Collector cutoff current	Ісво	-	-	0.5	μA	Vcb=100V
Emitter cutoff current	Іево	-	-	0.5	μΑ	VEB=4V
DC current transfer ratio	hfe	180	-	820	-	Vce=6V, Ic=2mA
Transition frequency	f⊤	-	140	-	MHz	Vce=12V, Ie=-2mA, f=100MHz *
Collector-emitter saturation voltage	VCE(sat)	-	-	0.5	V	Ic/I <sub>B</sub> =10mA/1mA

\*Transition frequency of the device

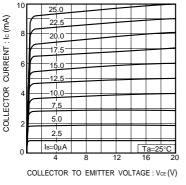
#### •External dimensions (Unit : mm)

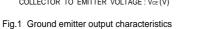


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# Transistors

#### •Electrical characteristics





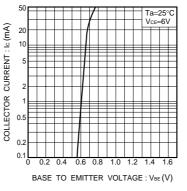


Fig.2 Ground emitter propagation characteristics

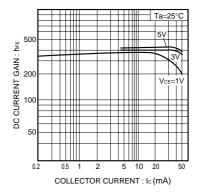
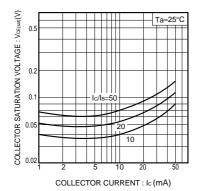
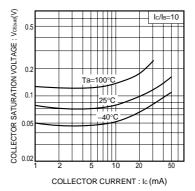


Fig.3 DC current gain vs. collector current





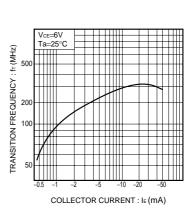


Fig.4 Collector-emitter saturation voltage vs. collector current ( I )

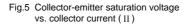
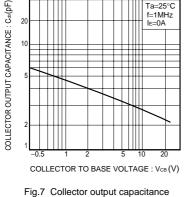
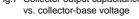
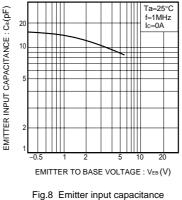
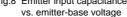


Fig.6 Gain bandwidth product vs. emitter current









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