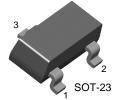


SEMICONDUCTOR®

## KST4123

## **General Purpose Transistor**



1. Base 2. Emitter 3. Collector

# **NPN Epitaxial Silicon Transistor**

## Absolute Maximum Ratings $T_a=25^{\circ}C$ unless otherwise noted

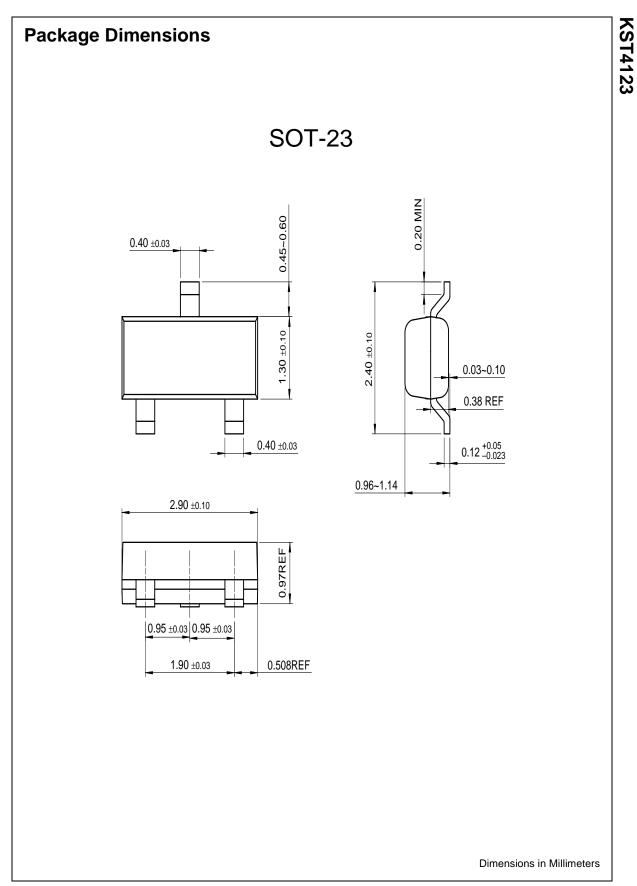
Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
c	Collector Current	200	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	150	°C
R <sub>TH</sub> (j-a)	Thermal Resistance junction to Ambient	357	°C/W

### **Electrical Characteristics** $T_a=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> =10μA, I <sub>E</sub> =0	40		V
BV <sub>CEO</sub>	* Collector-Emitter Breakdown Voltage	I <sub>C</sub> =1mA, I <sub>E</sub> =0	30		V
$BV_{EBO}$	Emitter-Base Breakdown Voltage	I <sub>E</sub> =10μA, I <sub>C</sub> =0	5		V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> =20V, I <sub>E</sub> =0		50	nA
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> =3V, I <sub>C</sub> =0		50	nA
h <sub>FE</sub>	* DC Current Gain	V <sub>CE</sub> =1V, I <sub>C</sub> =2mA	50	150	
		V <sub>CE</sub> =1V, I <sub>C</sub> =50mA	25		
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA		0.3	V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA		0.95	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> =20V, I <sub>C</sub> =10mA, f=100MHz	250		MHz
C <sub>ib</sub>	Input Capacitance	V <sub>BE</sub> =0.5V, I <sub>C</sub> =0, f=100KHz		8	pF
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =5V, I <sub>E</sub> =0, f=100KHz		4	pF
NF	Noise Figure	V <sub>CE</sub> =5V, I <sub>C</sub> =100μA, R <sub>S</sub> =1KΩ Noise Bandwidth=10Hz to 15.7KHz		6	dB

\* Pulse Test: PW≤300µs, Duty Cycle≤2%





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Rev. A2, November 2002

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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