

P-Channel 60V (D-S) MOSFET

General Description

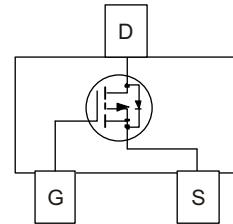
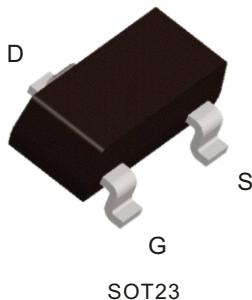
This miniature surface mount MOSFET uses advanced Trench process, low $R_{DS(on)}$ assures minimal power loss and energy convert, which makes this device ideal for use in power management circuit.

Applications

- Load switch
- DC-DC converters
- Power management

Features

- $V_{DS}(V) = -60V$
- $I_D(A) = -2.7A$ ($V_{GS} = -10V$)
- $R_{DS(on)} = 110\text{ m}\Omega$ @ $V_{GS} = -10V$
- $R_{DS(on)} = 160\text{ m}\Omega$ @ $V_{GS} = -4.5V$
- Low gate charge
- Fast switching speed
- High performance trench technology



SOT23

Absolute Maximum Ratings ($T_A = 25^\circ C$ Unless Otherwise Noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	I_D	-2.7	A
		-2.2	
Pulsed Drain Current ^b	I_{DM}	-11	
Continuous Source Current (Diode Conduction) ^a	I_S	-1.0	A
Power Dissipation ^a	P_D	1.4	W
		1.0	
Operating Junction and Storage Temperature Range	T_J, T_{Stg}	-55 to 150	°C

Thermal Resistance Ratings

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	90	°C/W
		130	



Package Outlines and Ordering Information

Device	Device Marking	Reel Size	Tape Width	Quantity
MI3461	S60P	7"	8mm	3000 units

Specifications (TA = 25°C Unless Otherwise Noted)

Parameter	Symbol	Test Conditions	Limits			Units
			Min	Typ	Max	
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	-60			V
Gate-Threshold Voltage	VGS(th)	VDS=VGS, ID = -250 uA	-1.0	-2.0	-3.0	
Gate-Body Leakage	IGSS	VDS=0V, VGS=±20V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=-60V, VGS=0V			-1	uA
		VDS=-60V, VGS=0V, TJ=55°C			-10	
On-State Drain Current ^c	ID(on)	VDS=-20V, VGS=-4.5V	-11			A
Drain-Source On-Resistance ^c	RDS(on)	VGS=-10V, ID=-3.5A		88	110	
		VGS=-4.5 V, ID=-2.0A		120	160	mΩ
Forward Transconductance ^c	g _{fs}	VDS=-5V, ID=-3.5A		12		S
Diode Forward Voltage	VSD	IS=-1.0A, VGS=0V		-0.8	-1.2	V
Dynamic						
Input Capacitance	C _{iss}	VDS =-30V, VGS =0V f=1MHz		745		pF
Output capacitance	C _{oss}			69		
Reverse Transfer Capacitance	C _{rss}			42		
Switching						
Total Gate Charge	Q _g	VDS=-30V, VGS=-10V ID =-2.7A		13.5		nC
Gate-Source Charge	Q _{gs}			1.5		
Gate-Drain Charge	Q _{gd}			3.2		
Turn-On Delay Time	t _{d(on)}	VDS=-30V, ID=-1.0A, RG=6 ohm, VGEN=-4.5V		12		ns
Rise Time	t _r			12		
Turn-Off Delay Time	t _{d(off)}			65.8		
Fall-Time	t _f			22		

Notes: a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

c. Pulse test: PW <= 300us duty cycle <= 2%.



Typical Electrical and Thermal Characteristics

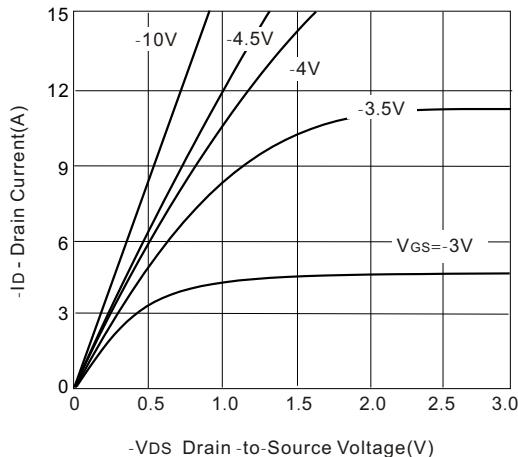


Figure 1: Output Characteristics

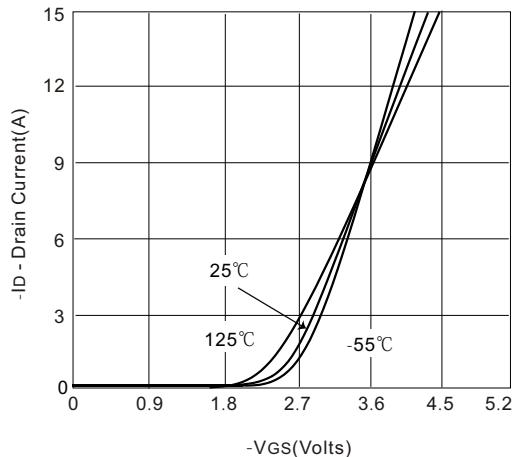


Figure 2: Transfer Characteristics

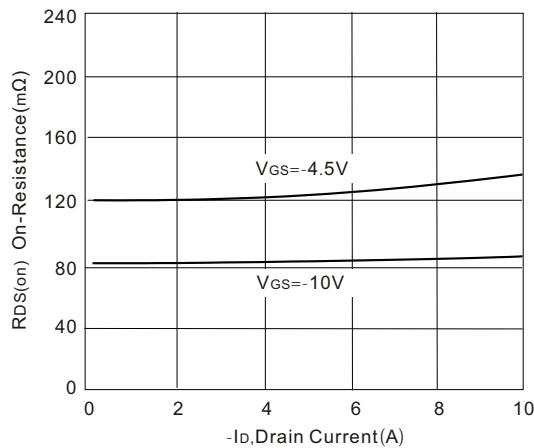


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

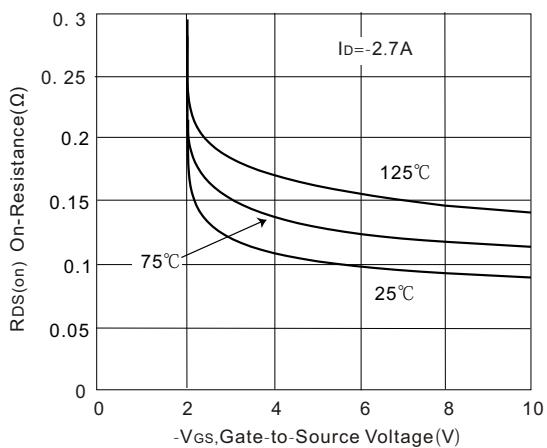


Figure 4: On-Resistance vs. Gate-Source Voltage

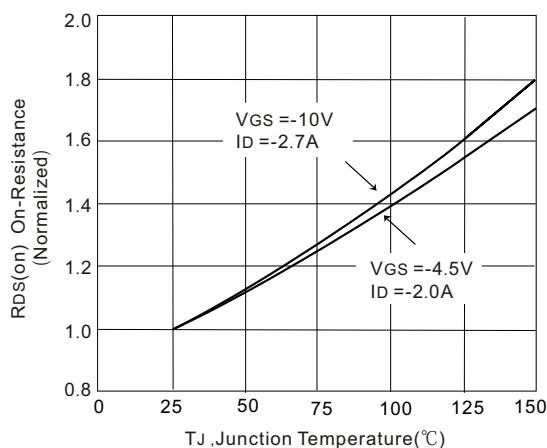


Figure 5: On-Resistance Variation With Temperature

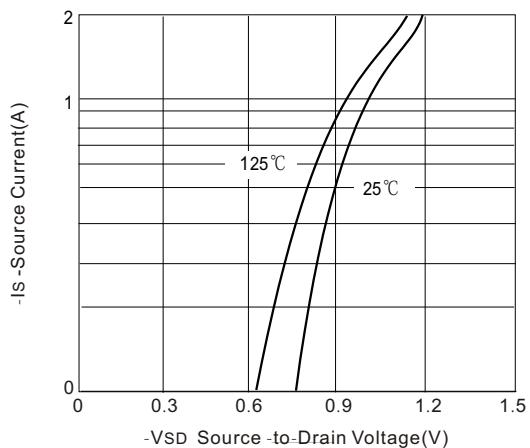


Figure 6: Source-Drain Forward Voltage



Typical Electrical and Thermal Characteristics

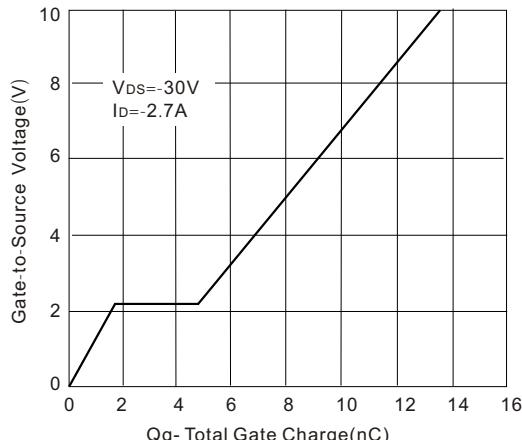


Figure 7: Gate - to - Source and
Drain - to - Source Voltage vs. Total Charge

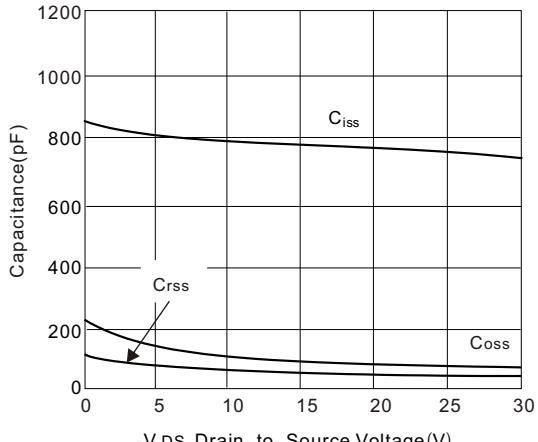


Figure 8: Capacitance

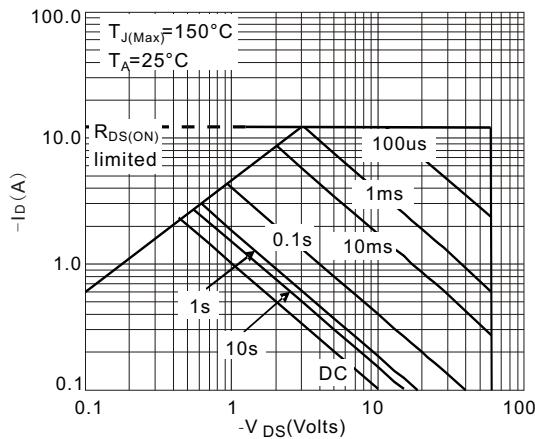


Figure 9: Maximum Forward Biased Safe
Operating Area (Note d)

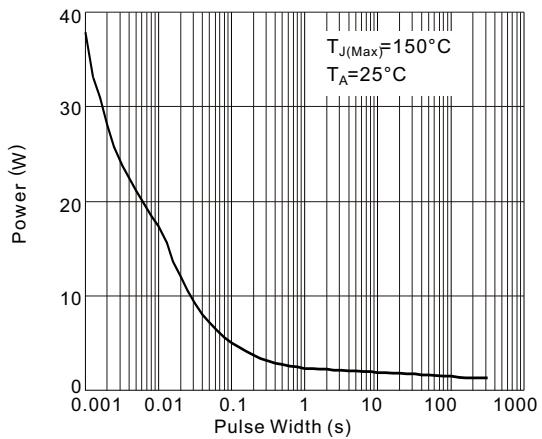


Figure 10: Single Pulse Power Rating Junction-to-
Ambient (Note d)

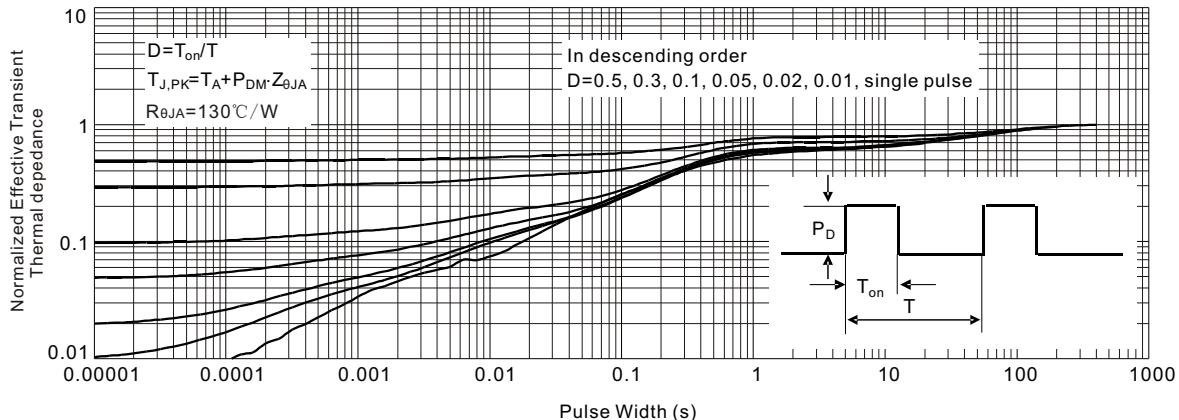


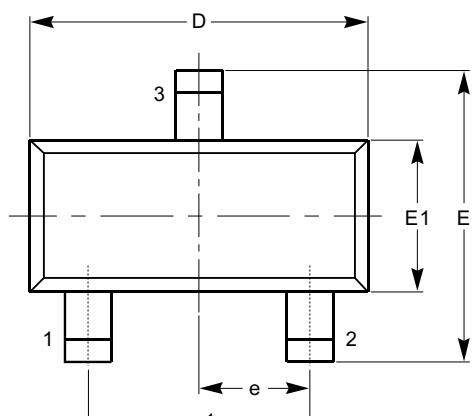
Figure 11: Normalized Maximum Transient Thermal Impedance

Note d: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with TA=25°C. The SOA curve provides a single pulse rating.



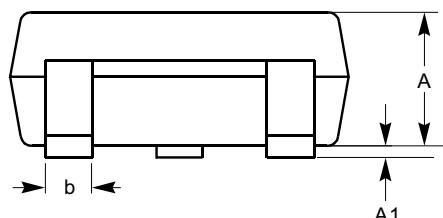
Package Outline

SOT23_3Lead

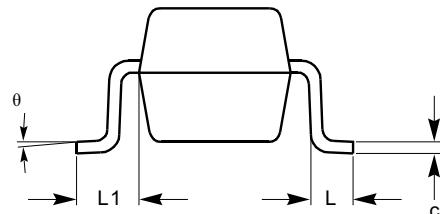


TOP VIEW

Unit: mm			
Symbol	Min	Nom	Max
A	0.70	1.00	1.15
A1	0.00	---	0.13
b	0.30	0.40	0.50
c	0.08	0.13	0.20
D	2.80	2.90	3.10
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95 BSC		
e1	1.90 BSC		
L	0.40 REF		
L1	0.54 REF		
θ	0°	5°	8°



SIDE VIEW



END VIEW

Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Package body sizes exclude mold flash and gate burrs.
- (3) Complies with JEDEC TO-236.

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