# MT3401

# P-Channel Enhancement Mode Field Effect Transistor

## FEATURES

- Super high dense cell design for low RDS(ON)
- Rugged and reliable
- Simple drive requirement
- SOT-23 package

PRODUCT S	PRODUCT SUMMARY		
Vdss	ID $RDS(ON)(m\Omega)$		
-30V	-5.6A	45@ VGS=-10V	
		65 @ VGS=-4.5V	



NOTE: The MT3401 is available in a lead-free package



## ABSOLUTE MAXIMUM RATINGS ( $T_A=25^{\circ}C$ unless otherwise noted)

Parameter Sym	bol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	VGS	±20	V
Drain Current-Continuous <sup>a</sup> @Tj=125°C	ID	-5.6	А
- Pulse $d^{b}$	Ідм	-25	Α
Drain-source Diode Forward Current <sup>a</sup>	Is	-1.5	А
Maximum Power Dissipation <sup>a</sup>	PD	1.5	W
Operating Junction and Storage Temperature Range	Tj,Tstg	-55 to 150	°C

#### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to Ambient <sup>a</sup> Rth	JA	90	°C/W
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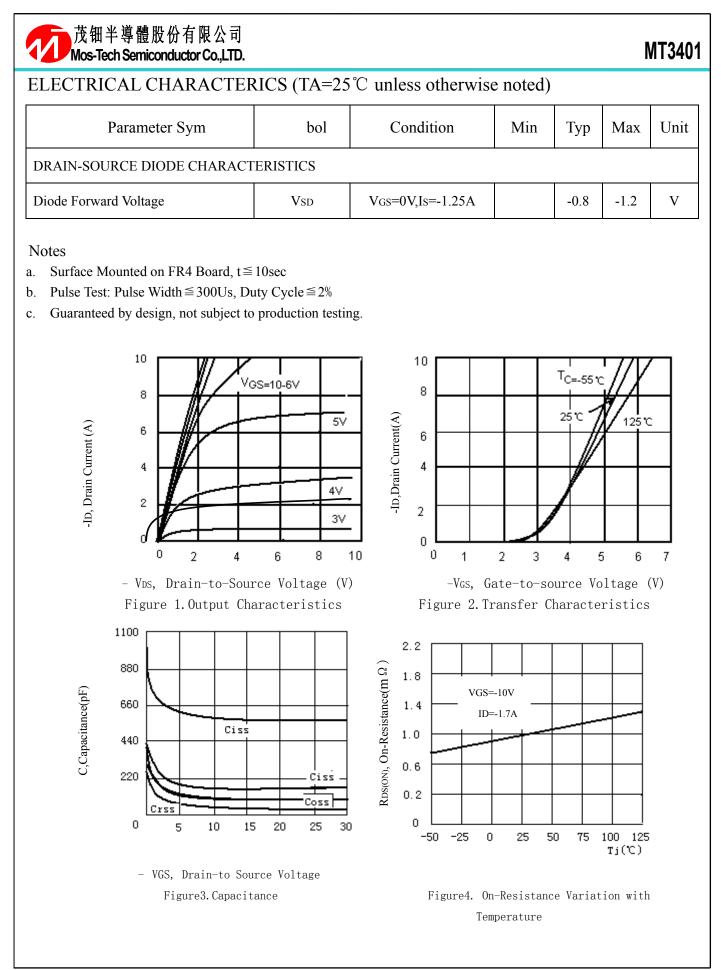


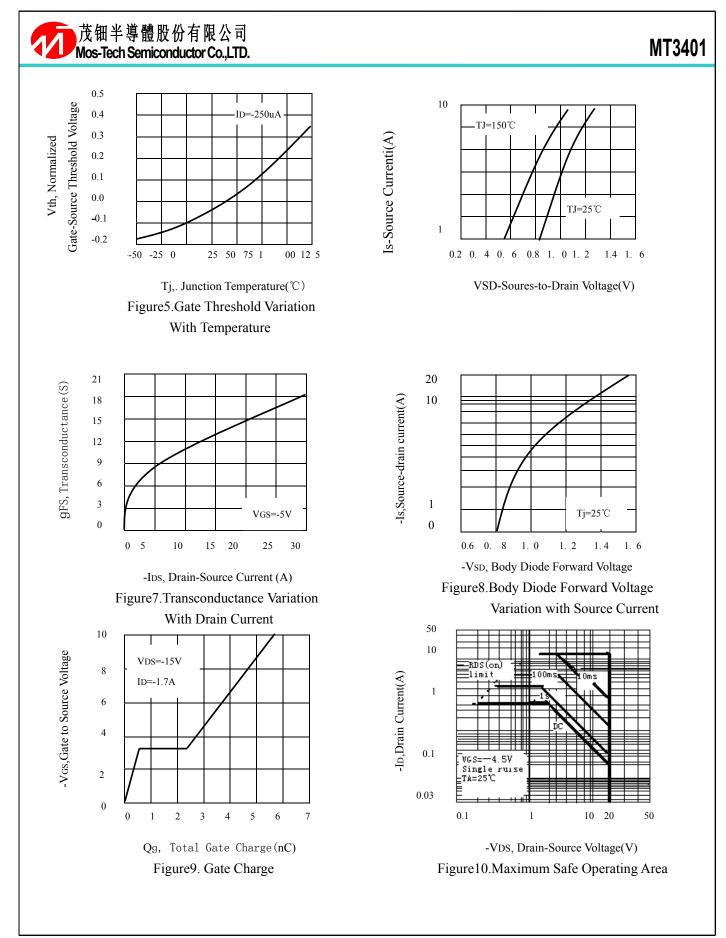
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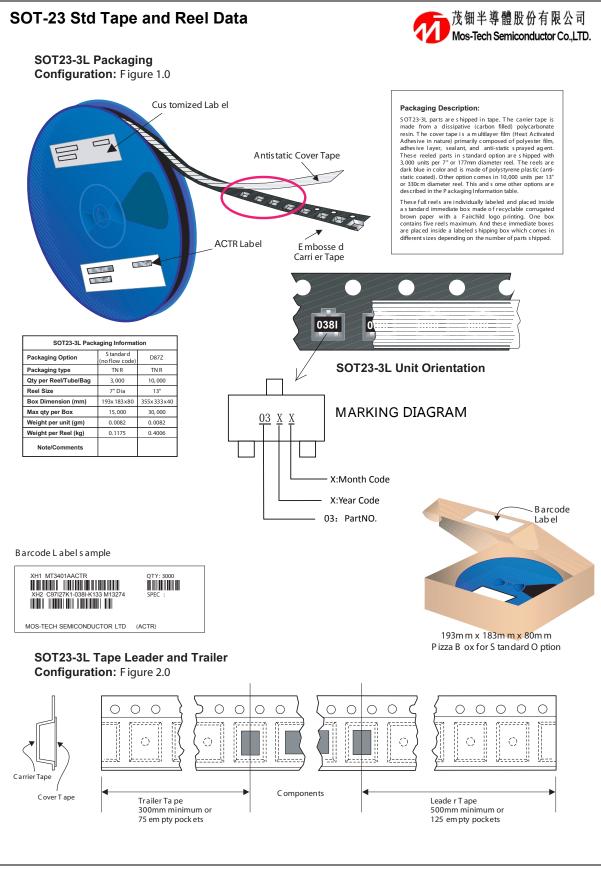
## ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter Sym	bol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	<u> </u>					•
Drain-Source Breakdown Voltage	BVDSS	VGS=0V,ID=-250µA		-30		V
Zero Gate Voltage Drain Current	IDSS	VDS=-30V,VGS=0V			1	μA
Gate-Body Leakage	Igss	VGS=±10V,VDS=0V			±100	nA
ON CHARACTERITICS						
Gate Threshold Voltage	VGs(th) V	DS=VGS,ID=-250µA	-1.2		-2.0	V
Durin Gaussi On State Deviations	Drawn	Vgs=-10V,ID=-4.6A		45	50	mΩ
Drain-Source On-State Resistance	Rds(on)	VGS=-4.5V,ID=-3.0A		65	70	
Forward Transconductance	gFS	Vgs=-10V,Id=-1.7A		17		S
DAYNAMIC CHARACTERISTICS	<u> </u>					•
Input Capacitance	Ciss			1226		pF
Output Capacitance	Coss	VDS=-15V,VGS=0V f=1.0MHz		187		pF
Reverse Transfer Capacitance	Crss			91		pF
SWITCHING CHARACTERISISTICS			-	•		1
Turn-On Delay Time	tD(ON) VDD=-15V	VDD=-15V		5.9		ns
Rise Time	tr	ID=-1.0A, VGEN=-10V RL=150hm RGEN=60hm		6.9		ns
Turn-Off Delay Time	td(off)			48		ns
Fall Time	tf			16		ns
Total Gate Charge	Qg			9.8		nC
Gate-Source Charge	Qgs	VDS=-15V,ID=-1.7A VGS=-10V		1.8		nC
Gate-Drain Charge	Qgd			4.5		nC



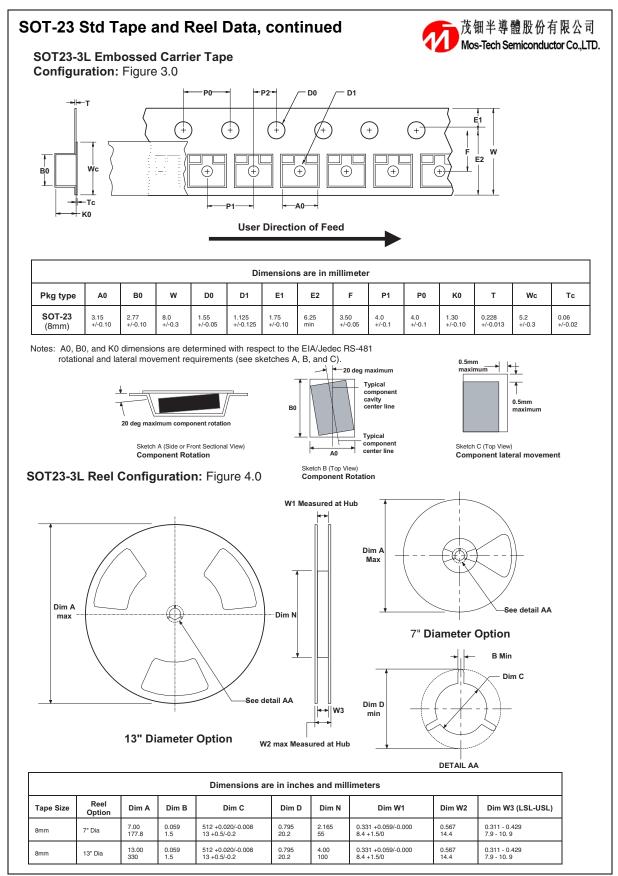


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