1.8V Drive Nch+SBD MOSFET QS5U34

Structure

Silicon N-channel MOSFET Schottky Barrier DIODE

Features

- 1) The QS5U34 combines Nch MOSFET with a Schottky barrier diode in a single TSMT5 package.
- 2) Low on-state resistance with fast switching.
- 3) Low voltage drive (1.8V).
- 4) The Independently connected Schottky barrier diode has low forward voltage.

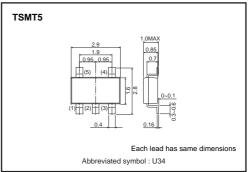
Applications

Load switch, DC / DC conversion

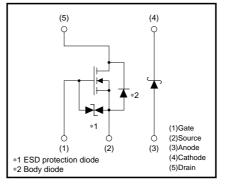
Packaging specifications

	Package	Taping	
Туре	Code	TR	
	Basic ordering unit (pieces)	3000	
QS5U34		0	

•Dimensions (Unit : mm)



Equivalent circuit



Transistors

●Absolute maximum ratings (Ta=25°C)

<MOSFET>

Parameter	Symbol	Limits	Unit				
Drain-source voltage		VDSS	20	V			
Gate-source voltage		V _{GSS}	10	V			
Drain current	Continuous	lo	±1.5	A			
	Pulsed	I _{DP} *1	±3.0	A			
Source current	Continuous	ls	0.6	A			
(Body diode)	Pulsed	I _{SP} *1	2.4	A			
Channel temperature	Tch	150	°C				
Power dissipation	PD *3 0.9		W/ELEMENT				
<di></di>							
Repetitive peak reverse volt	Vrm	30	V				
Reverse voltage		VR	20	V			
Forward current		IF	0.5	A			
Forward current surge peak		I _{FSM} *2	2.0	A			
Junction temperature	Tj	150	°C				
Power dissipation	Pd *3	0.7	W/ELEMENT				
<mosfet and="" di=""></mosfet>							
Total power dissipation	P _D *3	1.25	W / TOTAL				
Range of Storage temperate	Tstg	-55 to +150	°C				
*1 But<10up Duty avala<19/ *2.60		tod on a coromia	board				

*1 Pw≤10 $\mu s,$ Duty cycle≤1% *2 60Hz-1cyc. *3 Mounted on a ceramic board

•Electrical characteristics (Ta=25°C)

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	10	μΑ	V _{GS} =10V / V _{DS} =0V
Drain-source breakdown voltage	V(BR) DSS	20	_	-	V	I _D =1mA, / V _{GS} =0V
Zero gate voltage drain current	IDSS	-	_	1	μΑ	V _{DS} =20V / V _{GS} =0V
Gate threshold voltage	VGS (th)	0.3	-	1.3	V	VDS=10V / ID=1mA
Static drain-source on-state resistance		-	130	180	mΩ	I _D =1.5A, V _{GS} =4.5V
	$R_{DS(on)}^*$	-	170	240	mΩ	I _D =1.5A, V _{GS} =2.5V
		-	220	310	mΩ	I _D =0.8A, V _{GS} =1.8V
Forward transfer admittance	Yfs *	1.6	-	-	S	Vos=10V, Io=1.5A
Input capacitance	Ciss	-	110	-	pF	V _{DS} =10V
Output capacitance	Coss	-	18	-	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	15	-	pF	f=1MHz
Turn-on delay time	td (on) *	-	5	-	ns	ID=1.0A
Rise time	tr *	-	5	-	ns	
Turn-off delay time	t _{d (off)} *	-	20	-	ns	VGs=4.5V R∟=10Ω
Fall time	t _f *	-	3	-	ns	R _G =10Ω
Total gate charge	Qg *	-	1.8	2.5	nC	V _{DD} ≒10V
Gate-source charge	Q _{gs} *	-	0.3	-	nC	V _{GS} =4.5V
Gate-drain charge	Q _{gd} *	-	0.3	_	nC	II

<MOSFET>Body diode (source-drain)

Forward voltage	Vsd	-	-	1.2	V	Is=0.6A / V _{GS} =0V

<Di>

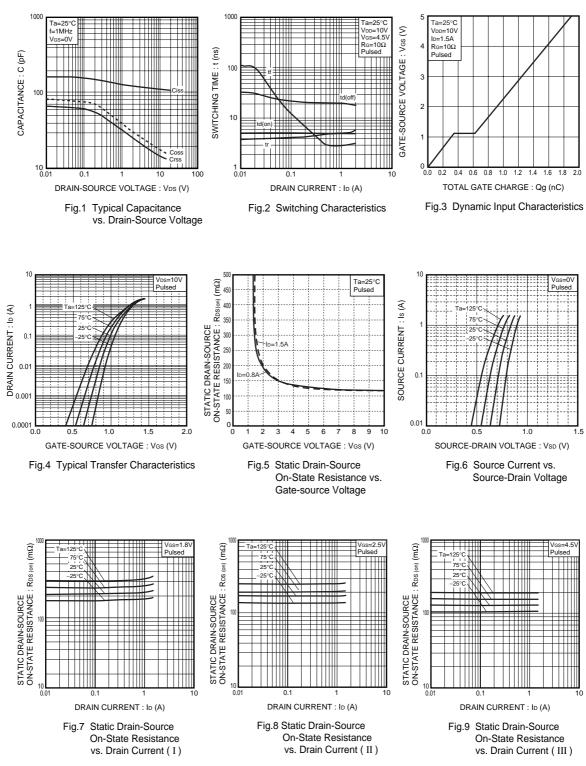
Forward voltage	VF	-	-	0.36	V	IF=0.1A
		_	-	0.47	V	IF=0.5A
Reverse current	IR	-	-	100	μA	VR=20V



Transistors

•Electrical characteristic curves

<MOSFET>



Transistors

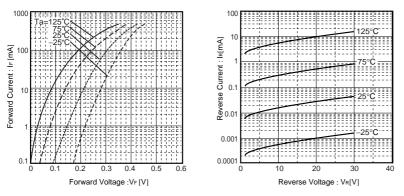


Fig.10 Forward Temperature Characteristics Fig.11 Reverse Temperature Characteristics

Notice

- SBD has a large reverse leak current compared to other type of diode. Therefore; it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway. This built-in SBD has low V_F characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.
- 2. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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