

AEC-Q101 Qualified

4V Drive Nch MOSFET

RHU002N06FRA

Structure

Silicon N-channel **MOSFET** transistor

Features

- 1) Low on-resistance. 2) High ESD. 3) High-speed switching. 4) Low-voltage drive (4V).
- 5) Drive circuits can be simple.
- 6) Parallel use is easy.

Applications

Switching

Packaging specifications

	Package	Taping
	Code	T106
Туре	Basic ordering unit (pieces)	3000
RHU002N06	RHU002N06FRA	

•Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		Vdss	60	V
Gate-source voltage		Vgss	±20	V
Drain current	Continuous	lD	±200	mA
	Pulsed	DP *1	±800	mA
Source current (Body diode)	Continuous	ls	200	mA
	Pulsed	Isp*1	800	mA
Total power dissipation		Pd *2	200	mW
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

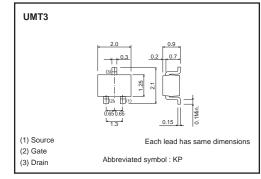
*1 Pw≤10µs, Duty cycle≤1%
*2 Each terminal mounted on a recommended

•Thermal resistance

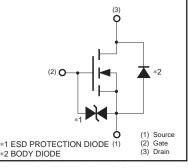
Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	625	°C / W
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* With each pin mounted on the recommended land.

•Dimensions (Unit : mm)



Equivalent circuit



^{*} A protection diode has been built in between the gate and the source to protect against static electricity when the product is in use. Use the protection circuit when fixed voltages are exceeded.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions	
Gate leakage current	lgss	-	-	±10	μΑ	Vgs=±20V, Vds=0V	
Drain-source breakdown voltage	V (BR) DSS	60	_	-	V	ID=1mA, VGS=0V	
Drain cutoff current	Ibss	_	_	1	μΑ	VDS=60V, VGS=0V	
Gate threshold voltage	VGS (th)	1	_	2.5	V	Vos=10V, Io=1mA	
Drain-source on-state resistance	۰ *	-	1.7	2.4	0	ID=200mA, Vgs=10V	
	KDS (on)	_	2.8	4.0	Ω	ID=200mA, Vgs=4V	
Forward transfer admittance	I Y _{fs} I*	0.1	_	_	S	Vos=10V, Io=200mA	
Input capacitance	Ciss	_	15	-	pF	VDS=10V VGS=0V	
Output capacitance	Coss	_	8	_	pF		
Reverse transfer capacitance	Crss	_	4	-	pF	f=1MHz	
Turn-on delay time	${ m t}$ d (on) *	_	6	-	ns	_ I⊳=100mA, Vod≒30V Vss=10V	
Rise time	tr*	_	5	-	ns		
Turn-off delay time	${ m t}_{ m d}$ (off) *	-	12	_	ns	RL=300Ω RG=10Ω	
Fall time	tŕ*	_	95	_	ns		
Total gate charge	Qg*	_	2.2	4.4	nC	V _{DD} ≒30V	
Gate-source charge	Q _{gs} *	_	0.6	_	nC	V _{GS} =10V I⊳=200mA	
Gate-drain charge	Q _{gd} *	_	0.3	-	nC		

* Pulsed

•Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd*	_	-	1.2	V	Is=200mA, V _{GS} =0V
*Pulsed						

Electrical characteristic curves

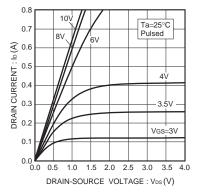
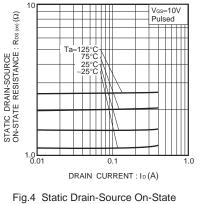


Fig.1 Typical Output Characteristics



Resistance vs. Drain Current (I)

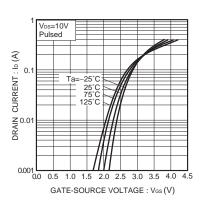
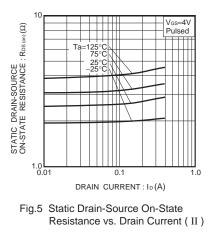
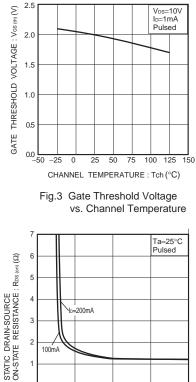


Fig.2 Typical Transfer Characteristics





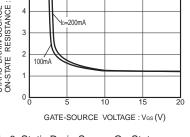
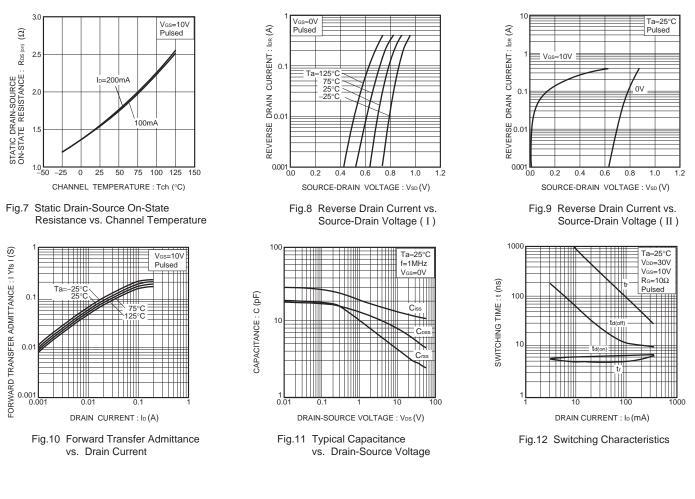


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

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•Switching characteristics measurement circuit

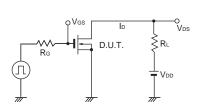


Fig.13 Switching time test circuit

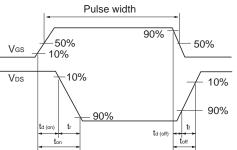


Fig.14 Switching time waveforms

	Notes
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