



P-Channel 150-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
-150	2.6 @ $V_{GS} = -10$ V	-0.52	4.2 nC
	2.7 @ $V_{GS} = -6$ V	-0.51	

FEATURES

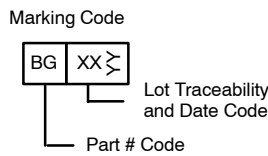
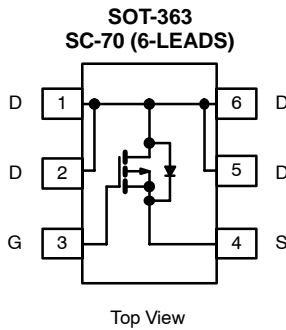
- TrenchFET® Power MOSFETS
- Small, Thermally Enhanced SC-70 Package
- Ultra Low On-Resistance

APPLICATIONS

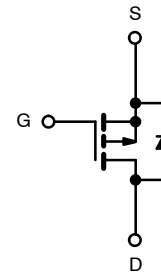
- Active Clamp Circuits in DC/DC Power Supplies



Product Is Completely Pb-free



Ordering Information: Si1411DH-T1—E3



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	5 secs	Steady State	Unit	
Drain-Source Voltage	V_{DS}	-150		V	
Gate-Source Voltage	V_{GS}	± 20			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	-0.52	-0.42	A
		$T_A = 85^\circ\text{C}$	-0.38	-0.3	
Pulsed Drain Current	I_{DM}	-0.8			
Continuous Diode Current (Diode Conduction) ^a	I_S	-1.3	-0.83		
Single Pulse Avalanche Current	$L = 0.1$ mH	I_{AS}	-2.1		
Single Pulse Avalanche Energy			E_{AS}	0.22	
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	1.56	1.0	W
		$T_A = 85^\circ\text{C}$	0.81	0.52	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 5$ sec	60	80	$^\circ\text{C/W}$
		Steady State	100	125	
Maximum Junction-to-Foot (Drain)	R_{thJF}	34	45		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

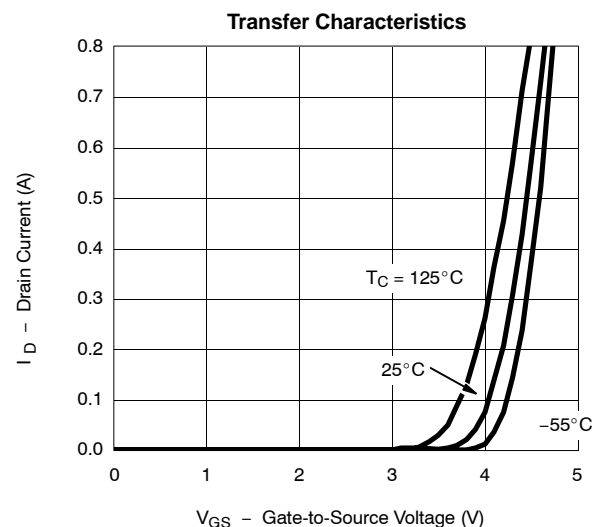
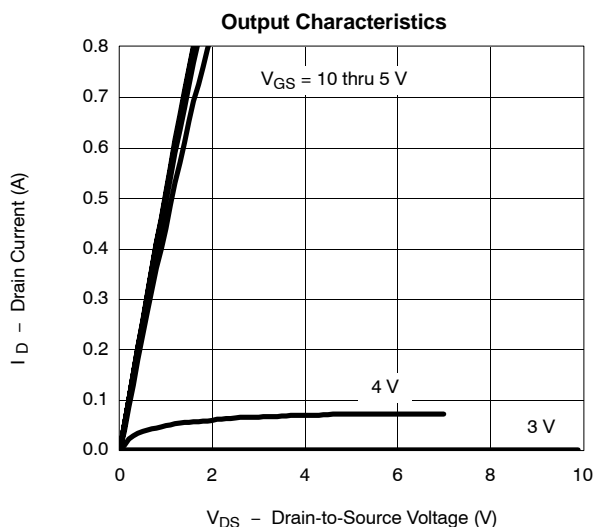
SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -100 μA	-2.5		-4.5	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -150 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -150 V, V _{GS} = 0 V, T _J = 85 °C			-5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -15 V, V _{GS} = -10 V	-0.8			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -10 V, I _D = -0.5 A		2.05	2.6	Ω
		V _{GS} = -6 V, I _D = -0.5 A		2.14	2.7	
Forward Transconductance ^a	g _{fs}	V _{DS} = -10 V, I _D = -0.5 A		1.5		S
Diode Forward Voltage ^a	V _{SD}	I _S = -1.4 A, V _{GS} = 0 V		-0.80	-1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -75 V, V _{GS} = -10 V, I _D = -0.5 A		4.2	6.3	nC
Gate-Source Charge	Q _{gs}			0.9		
Gate-Drain Charge	Q _{gd}			1.3		
Gate Resistance	R _g	f = 1.0 MHz		8.5		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = -75 V, R _L = 75 Ω I _D ≅ -1 A, V _{GEN} = -4.5 V, R _g = 6 Ω		4.5	7	ns
Rise Time	t _r			11	17	
Turn-Off Delay Time	t _{d(off)}			9	14	
Fall Time	t _f			11	17	
Reverse Recovery Time	t _{rr}	I _F = -0.5 A, di/dt = 100 A/μs		36	55	ns
Body Diode Reverse Recovery Charge	Q _{rr}			65	100	

Notes

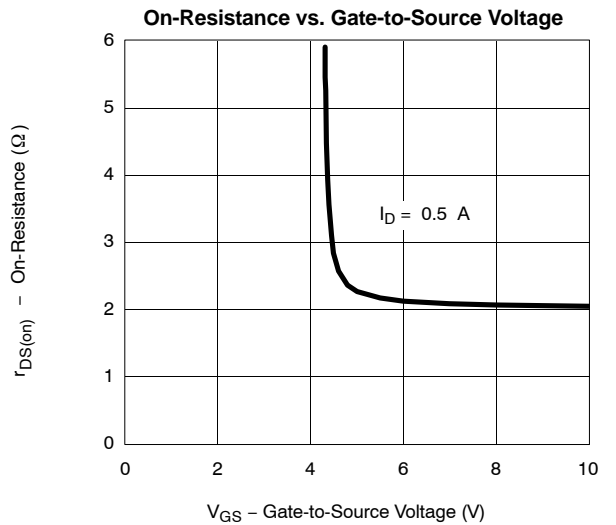
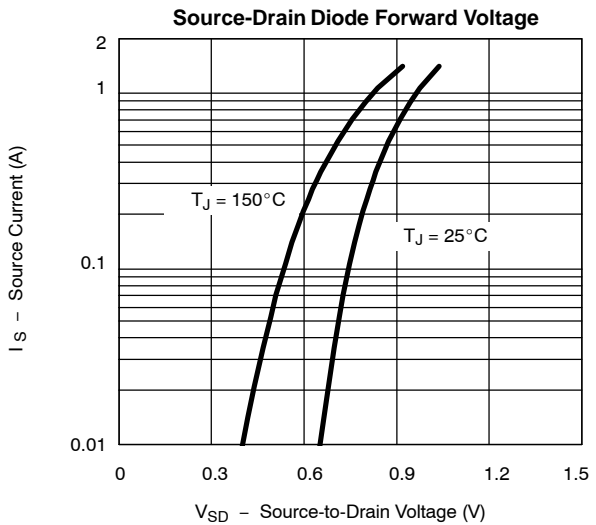
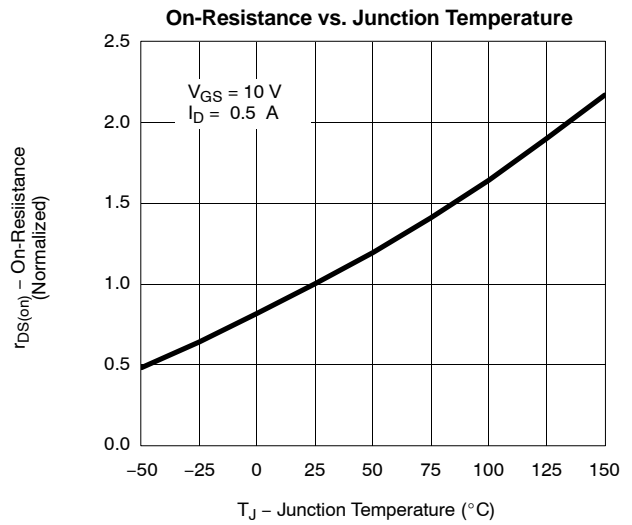
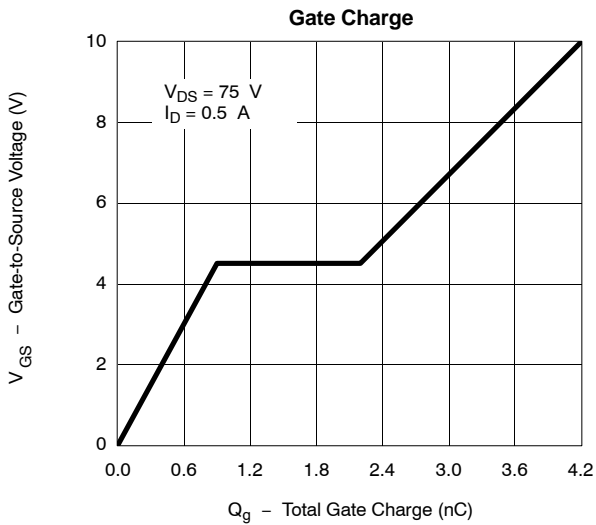
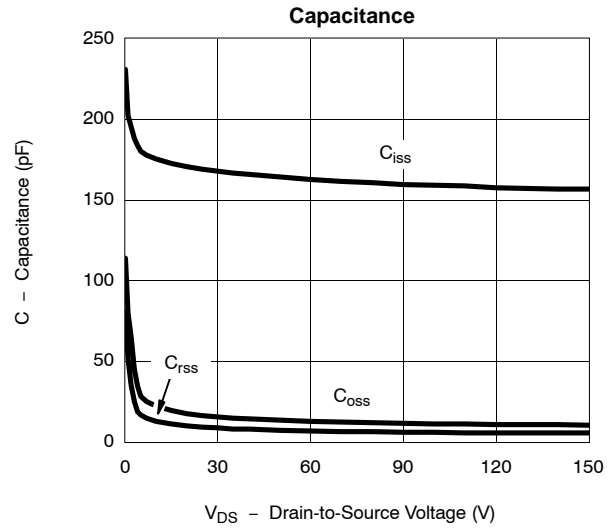
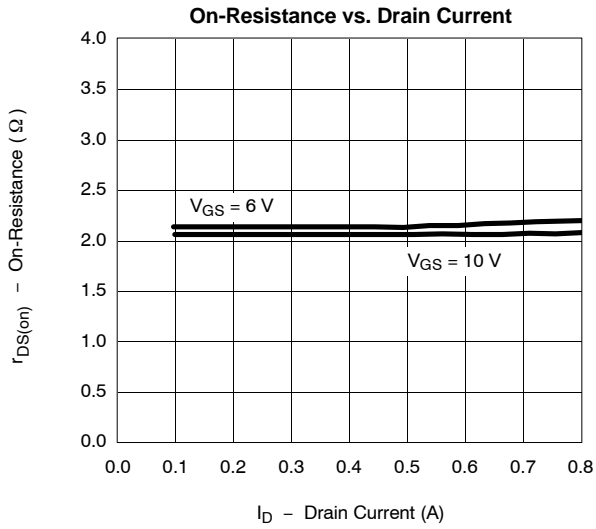
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

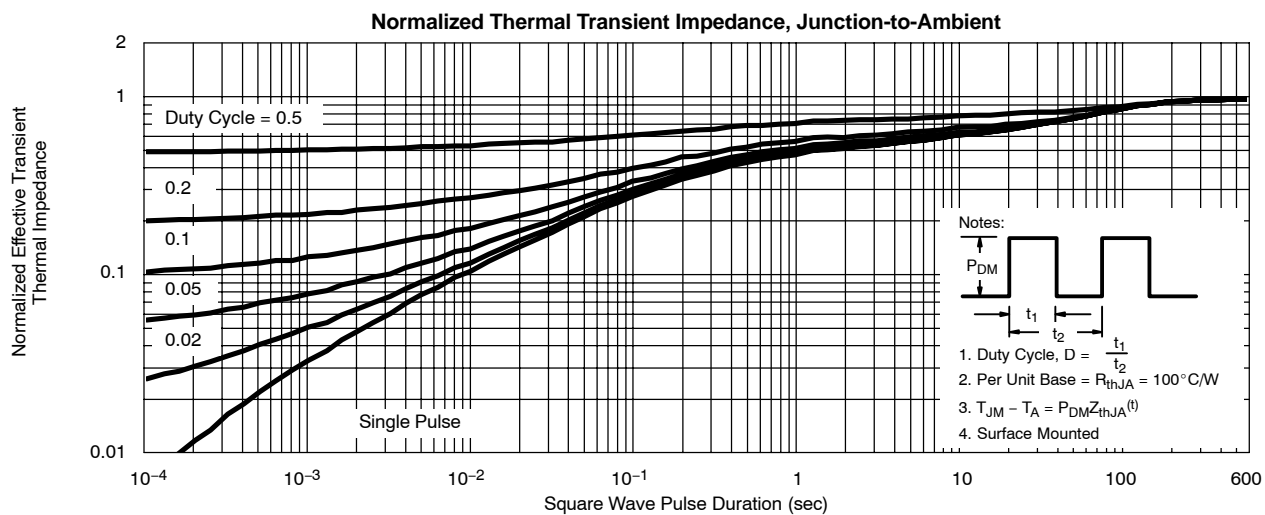
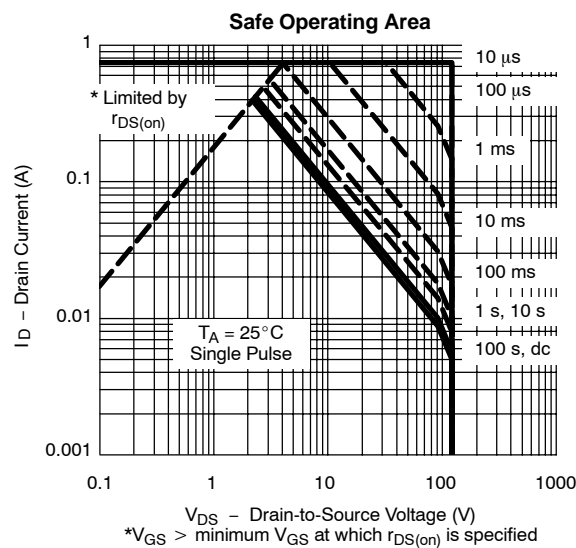
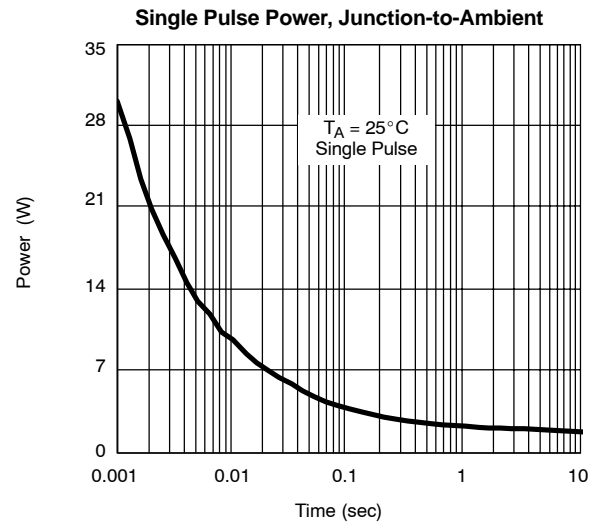
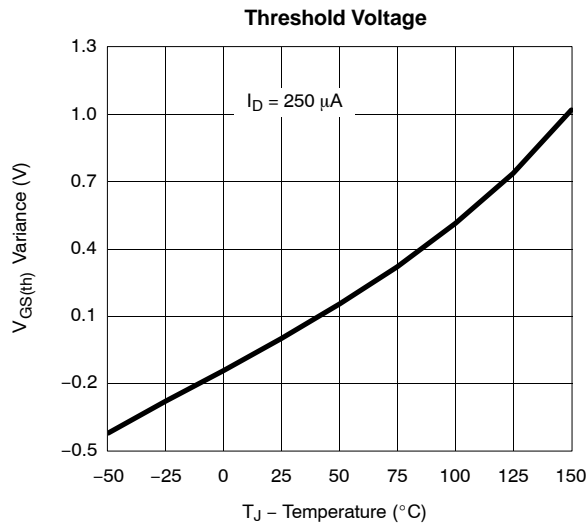


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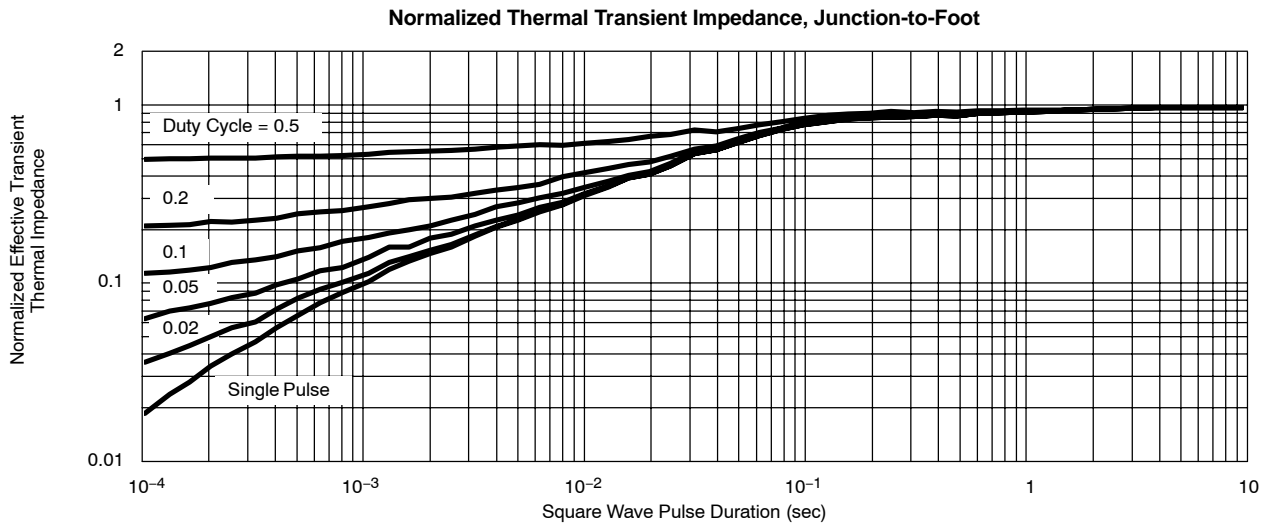


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