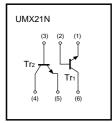
High transition frequency (dual transistors)

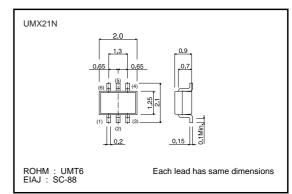
Features

- 1) Two 2SC4713K chips in a UMT package.
- 2) Very low output-on resistance. (Ron)
- 3) Low capacitance.

Equivalent circuits



•Dimensions (Unit : mm)



•Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	mits Unit	
Collector-base voltage	Vсво	12	V	
Collector-emitter voltage	VCEO	6	V	
Emitter-base voltage	Vebo	3	V	
Collector current	lc	50	mA	
Collector power dissipation	Pc	150	mW *	
Junction temperature	Tj	150	٥C	
Storage temperature	Tstg	-55 to +150	°C	

* 120mW per element must not be exceeded.

Package, marking, and packaging specifications

UMX21N
UMT6
X21
TR
3000

Electrical characteristics (Ta=25°C)

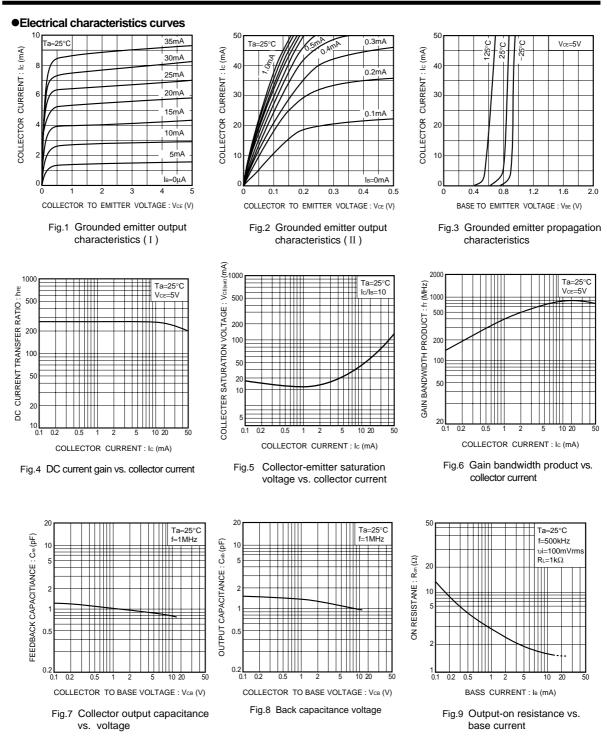
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	12	-	-	V	Ic=10μA
Collector-emitter breakdown voltage	BVCEO	6	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	3	-	-	V	Iε=10μA
Collector cutoff current	Ісво	-	-	0.5	μΑ	V _{CB} =10V
Emitter cutoff current	Іево	-	-	0.5	μΑ	V _{EB} =2V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.3	V	Ic/IB=10mA/1mA
DC current transfer ratio	hfe	270	-	560	-	VcE/Ic=5V/10mA
Transition frequency	fт	300	800	-	MHz	Vce=5V, Ie= -10mA, f=200MHz
Output capacitance	Cob	-	1	1.7	pF	Vcb=10V, IE=0A, f=1MHz
Output-on resistance	Ron	-	2	-	Ω	IB=3mA, VI=100mVrms, f=500kHz

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



UMX21N

Transistors



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